

# Transient Stability Analysis and Coordinated Control Strategy of DC-link Voltage for PLL-based Grid-connected DFIG System During Asymmetrical Grid Faults

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**Abstract**—The transient behavior of DC-link voltage (DCV) significantly affects the low-voltage ride-through for phase-locked loop (PLL)-based grid-connected doubly-fed induction generator (DFIG) systems. This study investigates the DCV transient behavior of a PLL-based DFIG system under asymmetrical grid faults. First, by considering the coupling characteristics of positive and negative sequence (PNS) components, a nonlinear large-signal model of DCV is developed. Furthermore, the transient characteristics of DCV under varying parameters are analyzed using phase trajectory diagrams. In addition, the transient stability (TS) mechanism of DCV during asymmetrical faults is examined through an energy function approach. The analysis indicates that the transient instability of DCV is primarily associated with the control characteristics of PNS PLLs, while the TS level of DCV is mainly determined by the power coordination control between the rotor side converter and grid side converter. Moreover, a coordinated control strategy is proposed to enhance the TS of DCV under asymmetrical grid faults. Finally, both simulation and experimental results are presented to validate the theoretical analysis and the effectiveness of the proposed strategy.

**Index Terms**—Transient stability, DC-link voltage, doubly-fed induction generator, asymmetrical grid faults, stability control strategy.

## NOMENCLATURE

### A. Abbreviations

PLL	phase-locked loop
DFIG	doubly-fed induction generator
DCV	DC-link voltage
RSC	rotor side converter
GSC	grid side converter
REPF	renewable energy power farm
PS	positive sequence
NS	negative sequence

### B. Variables

$U_f$	vectors of faulted grid voltage
$U_s$	stator voltage
$I$	output current of DFIG
$R_s, L_s$	stator resistance and inductance
$R_r, L_r$	rotor resistance and inductance
$L_m$	mutual inductance
$\theta^+$	output angle of positive-sequence PLL
$\theta^-$	output angle of negative-sequence PLL
$\omega_{\text{pll}}^+$	output angular frequency of positive-sequence PLL
$\omega_{\text{pll}}^-$	output angular frequency of negative-sequence PLL
$U_{\text{dc}}$	DC-link voltage of DFIG
$\delta^+$	angle difference between $U_s^+$ and $U_{\text{gf}}^+$
$\delta^-$	angle difference between $U_s^-$ and $U_{\text{gf}}^-$

### C. Subscripts

$d \pm, q \pm$	$d \pm / q \pm$ axis component
r, s, g	rotor, stator, and grid-side converter
f	values during low voltage ride through

### D. Superscripts

$\pm$	PS and NS component
*	reference value

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## I. INTRODUCTION

Recently, renewable energy generation technologies, particularly wind power, have grown rapidly worldwide due to their wide availability, extensive distribution, and environmental friendliness [1]. However, many renewable energy power farms (REPFs) are located in remote areas, where grid voltages are susceptible to asymmetrical drops caused by unbalanced three-phase loads or short-circuit faults. Given the limited anti-interference capability of power electronic devices, the complex voltage characteristics during faults can substantially affect the transient stability (TS) of REPF systems [2]. In particular, phase-locked loop (PLL)-based doubly-fed induction generator (DFIG) systems, whose stator is directly connected to the grid, are prone to rotor over-current, DC-link over-voltage, and even the loss of synchronous phenomenon [3], [4]. Therefore, it is important to further investigate the transient behavior of PLL-based DFIG systems under asymmetrical grid faults.

TS of PLL-based DFIG systems under grid faults has been investigated. References [5]–[7] examine the transient synchronization characteristics of REPF during various stages of grid faults and outline the physical mechanisms that allow REPF systems to maintain transient synchronism with the grid. However, the impact of the transient behavior for DC-link voltage (DCV) on the TS characteristics of REPF systems has not been addressed in the studies. Since the DC-link capacitance in DFIG systems serves as a key energy storage element between the rotor side converter (RSC) and grid side converter (GSC) [8], [9], DCV may increase or decrease due to energy change in the DC capacitance during asymmetrical faults. Moreover, given that the control bandwidth of DCV is close to that of PLL [10], the dynamic behavior of DCV may further influence the TS of the PLL. Consequently, the instability of DCV can also induce transient instability for REPF systems.

To investigate the transient behavior of DCV in REPF during grid faults, references [11]–[13] explore the dynamic characteristics of DCV under grid fault conditions. However, these studies mainly focus on DCV behavior under symmetrical grid faults, whereas under asymmetrical grid faults, positive and negative sequence (PNS) components appear simultaneously in the DFIG system [14]. In addition, since the PNS networks are interconnected, the PNS components are coupled under asymmetrical faults [15]. Therefore, the transient characteristics for DCV in DFIG systems under asymmetrical grid faults are considerably more complex than symmetrical faults. Although previous work [16] has investigated the dynamic behavior of PNS components of REPF in PLL on the control timescale during asymmetrical faults, it did not consider the

transient behavior of DCV. As a result, the transient response of the system during asymmetrical faults requires further investigation.

Furthermore, several control strategies have been proposed to improve the TS of DCV in REPF during grid faults. References [17]–[19] introduce adaptive control methods to mitigate DCV fluctuations under both asymmetrical and symmetric grid voltage faults. However, these strategies do not incorporate a dual-sequence PLL, making it difficult to accurately identify the dominant factors affecting the TS of DCV and thereby limiting the suppression of their effects. In addition, the role of the integrator is not addressed in these methods, which further limits the transient performance of DCV [20], [21]. Particularly, under severe grid faults, the ability of DCV to maintain TS is weak. Therefore, it is essential to further study the effect of the integrator on the transient characteristics of DCV and develop an effective control strategy that enhances the TS of DCV in DFIG systems under asymmetrical grid faults.

To address the gaps mentioned above, this paper further investigates the transient instability forms and the TS mechanism of DCV for the PLL-based grid-connected DFIG system under asymmetrical faults. Based on this analysis, a coordinated control strategy is proposed to enhance the TS of PLL-based DFIG systems during asymmetrical grid faults. The main contributions are summarized as follows.

1) A novel transient modeling method of DCV for DFIG systems during asymmetrical faults is presented, which takes into account the transient characteristics of DCV, the dynamic behaviors of PNS components, and their coupling relationships.

2) The instability mechanism for DCV during asymmetrical grid faults is revealed. In addition, the dominant factors that influence the TS level of DCV are identified through the use of the phase trajectory diagram.

3) Considering the latest grid codes and the current capacity limits of converters, this paper proposes a novel TS control strategy to enhance both the response characteristics and TS level of DCV during asymmetrical grid faults. This is achieved through power coordination control between the RSC and GSC.

The organization of the rest of the paper is as follows. Section II establishes the nonlinear transient model in the DCV control timescale, while Section III presents the TS mechanism of DCV during asymmetrical grid faults. Section IV introduced a coordinated control strategy of DCV during symmetrical grid faults, and Section V verifies the analysis through simulation and experimental results. Finally, Section VI provides the conclusions of the study.

## II. MODELING OF GRID-CONNECTED DFIG SYSTEM IN DCV CONTROL TIME SCALE

During low-voltage ride-through (LVRT), to provide fast PNS reactive currents support to the system according to grid codes [22]–[24], the RSC of the DFIG system adopts the single current loop control strategy. Figure 1 illustrates the typical control diagram of a grid-connected PLL-based DFIG system during asymmetrical grid faults [15].

As shown in Fig. 1, the PLL-based DFIG system

contains several physical energy storage components, including the rotor, the DC capacitance, and the AC inductance. Since the transient response of DCV is considerably slower than that of the inner current, most studies [6], [12], [24] disregard the influence of the dynamic characteristics of the current loop on system TS. Therefore, this paper assumes that the output current vectors of the GSC and RSC can instantaneously follow their reference values. In addition, the dynamic characteristics of the stator and rotor flux, as well as the power loss of the DFIG system, are also neglected [14].

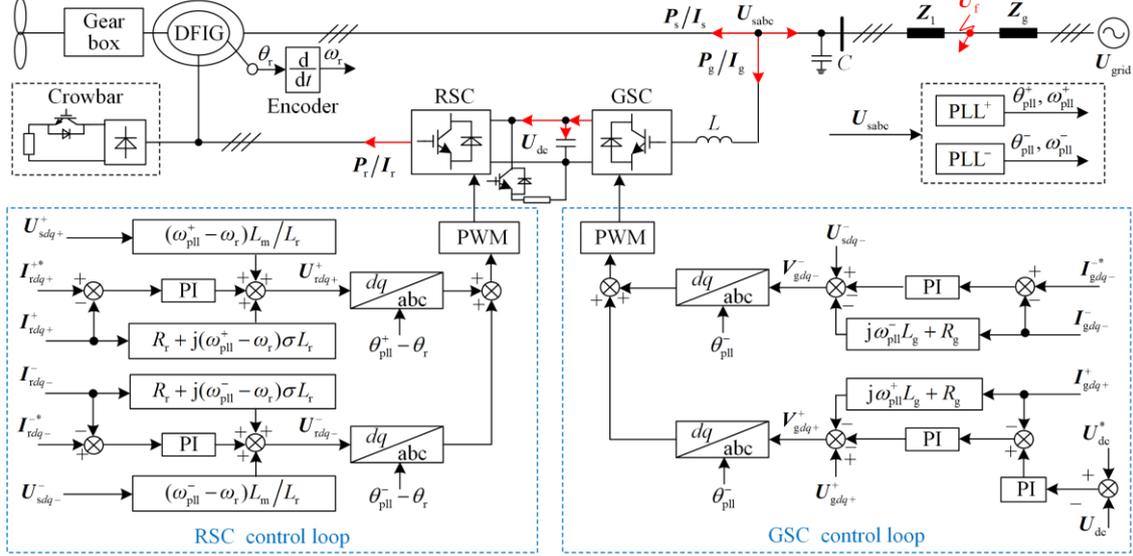


Fig. 1. Typical control structure of a PLL-based DFIG system during asymmetrical faults.

To further investigate the dynamic coupling behaviors of the PNS components [25] in the DFIG system within the DCV control timescale under asymmetrical faults, this paper establishes a dual-sequence synchronization large signal model in the PNS synchronous reference frames ( $dq\pm$ ), as shown as Fig. 2.

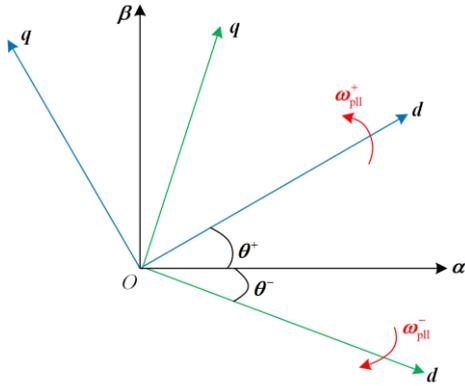


Fig. 2. PNS synchronous reference frames.

To ensure accurate tracking of the PNS voltage of the system, the PNS dual-sequence PLL control scheme proposed in [16] is adopted. Furthermore, the method introduced in [26] is applied to extract the PNS voltage

components separately. According to [16], the voltage and flux equations of the DFIG system in  $dq\pm$  are:

$$\begin{cases} U_{sdq+}^+ = -R_s I_{sdq+}^+ + \frac{d\Psi_{sdq+}^+}{dt} + j\omega_{pll}^+ \Psi_{sdq+}^+ \\ U_{rdq+}^+ = R_r I_{rdq+}^+ + \frac{d\Psi_{rdq+}^+}{dt} + j(\omega_{pll}^+ - \omega_r) \Psi_{rdq+}^+ \\ U_{sdq-}^- = -R_s I_{sdq-}^- + \frac{d\Psi_{sdq-}^-}{dt} + j\omega_{pll}^- \Psi_{sdq-}^- \\ U_{rdq-}^- = R_r I_{rdq-}^- + \frac{d\Psi_{rdq-}^-}{dt} + j(\omega_{pll}^- - \omega_r) \Psi_{rdq-}^- \end{cases} \quad (1)$$

$$\begin{cases} \Psi_{sdq+}^+ = -L_s I_{sdq+}^+ + L_m I_{rdq+}^+ \\ \Psi_{rdq+}^+ = -L_m I_{sdq+}^+ + L_r I_{rdq+}^+ \\ \Psi_{sdq-}^- = -L_s I_{sdq-}^- + L_m I_{rdq-}^- \\ \Psi_{rdq-}^- = -L_m I_{sdq-}^- + L_r I_{rdq-}^- \end{cases} \quad (2)$$

### A. Dual-sequence Synchronous Model of the DFIG System

During the pre-fault stage, the DFIG system operates under maximum power point tracking (MPPT) and unity power factor mode. When an asymmetrical short-circuit fault occurs, the voltage of the stator  $U_s$  is given as:

$$\mathbf{U}_s = \begin{cases} \mathbf{U}_{sN} e^{j\omega_g t}, & t < t_0 \\ \mathbf{U}_s^+ e^{j\omega_{pll}^+ t} + \mathbf{U}_s^- e^{j\omega_{pll}^- t}, & t \geq t_0 \end{cases} \quad (3)$$

where  $\mathbf{U}_{sN}$  represents the rated stator voltage; while  $\mathbf{U}_s^+$  and  $\mathbf{U}_s^-$  are the PNS components of the stator voltage during faults, respectively.

By neglecting the stator resistance  $R_s$  and combining (1)–(3), the relationships between stator and rotor current can be obtained as:

$$\begin{cases} \mathbf{I}_{sd+}^+ = \frac{L_m}{L_s} \mathbf{I}_{rd+}^+ \\ \mathbf{I}_{sq+}^+ = \frac{1}{L_s} \left( L_m \mathbf{I}_{rq+}^+ + \frac{|\mathbf{U}_s^+|}{\omega_{pll}^+} \right) \\ \mathbf{I}_{sd-}^- = \frac{L_m}{L_s} \mathbf{I}_{rd-}^- \\ \mathbf{I}_{sq-}^- = \frac{1}{L_s} \left( L_m \mathbf{I}_{rq-}^- + \frac{|\mathbf{U}_s^-|}{\omega_{pll}^-} \right) \end{cases} \quad (4)$$

Similarly, the mathematical model of the GSC under asymmetrical faults can be delivered as:

$$\begin{cases} \mathbf{U}_{gdq+}^+ = \mathbf{V}_{gdq+}^+ + (j\omega_{pll}^+ L_g + R_g) \mathbf{I}_{gdq+}^+ + L_g \frac{d\mathbf{I}_{gdq+}^+}{dt} \\ \mathbf{U}_{gdq-}^- = \mathbf{V}_{gdq-}^- + (j\omega_{pll}^- L_g + R_g) \mathbf{I}_{gdq-}^- + L_g \frac{d\mathbf{I}_{gdq-}^-}{dt} \end{cases} \quad (5)$$

### B. Transient Model in the DCV Control Timescale

In Fig. 1, the governing equation of the DCV can be expressed as:

$$C \frac{dU_{dc}}{dt} = \frac{P_g}{U_{dc}} - \frac{P_r}{U_{dc}} \quad (6)$$

where  $P_s$  is the active power absorbed by GSC; while  $P_r$  is the active power released by RSC; and  $C$  is the value of DC-link capacitor.

According to [11], the relationship between  $P_g$  and  $P_r$  is:

$$\begin{cases} P_{r+} = s_1 P_{s+} \\ P_{r-} = s_2 P_{s-} \\ s_1 = (\omega_{pll}^+ - \omega_g) / \omega_{pll}^+ \\ s_2 = (\omega_{pll}^- - \omega_g) / \omega_{pll}^- \end{cases} \quad (7)$$

The stator power of the DFIG system is:

$$\mathbf{S}_s = \frac{3}{2} \left( \mathbf{U}_{sdq+}^+ e^{j\omega_{pll}^+ t} + \mathbf{U}_{sdq-}^- e^{j\omega_{pll}^- t} \right) \times \left( \mathbf{I}_{sdq+}^+ e^{j\omega_{pll}^+ t} + \mathbf{I}_{sdq-}^- e^{j\omega_{pll}^- t} \right) \quad (8)$$

Based on (8),  $P_s$  can be written as:

$$P_s = \frac{3}{2} \begin{bmatrix} u_{sd+}^+ i_{sd+}^+ + u_{sd-}^- i_{sd-}^- + u_{sq+}^+ i_{sq+}^+ + u_{sq-}^- i_{sq-}^- \\ \left( u_{sd-}^- i_{sd+}^+ + u_{sq-}^- i_{sq+}^+ + u_{sd+}^+ i_{sd-}^- + u_{sq+}^+ i_{sq-}^- \right) \cos(\theta^+ + \theta^-) + \\ \left( u_{sq-}^- i_{sd+}^+ - u_{sd-}^- i_{sq+}^+ - u_{sq+}^+ i_{sd-}^- + u_{sd+}^+ i_{sq-}^- \right) \sin(\theta^+ + \theta^-) \end{bmatrix} \quad (9)$$

Substituting (4) into (9) yields:

$$P_r = \frac{3L_m}{2L_s} \times \begin{bmatrix} \left( u_{sd+}^+ + u_{sd-}^- \cos(\theta^+ + \theta^-) + \right. \\ \left. u_{sq-}^- \sin(\theta^+ + \theta^-) \right) \times s_1 i_{rd+}^{+*} + \\ \left( u_{sq+}^+ + u_{sq-}^- \cos(\theta^+ + \theta^-) - \right. \\ \left. u_{sd-}^- \sin(\theta^+ + \theta^-) \right) \times \left( i_{rq+}^+ + \frac{|\mathbf{U}_s^+|}{L_m \omega_{pll}^+} \right) s_1 + \\ \left( u_{sd-}^- + u_{sd+}^+ \cos(\theta^+ + \theta^-) - \right. \\ \left. u_{sq+}^+ \sin(\theta^+ + \theta^-) \right) \times s_2 i_{rd-}^{+*} + \\ \left( u_{sq-}^- + u_{sq+}^+ \cos(\theta^+ + \theta^-) + \right. \\ \left. u_{sd+}^+ \sin(\theta^+ + \theta^-) \right) \times \left( i_{rq-}^- + \frac{|\mathbf{U}_s^-|}{L_m \omega_{pll}^-} \right) s_2 \end{bmatrix} \quad (10)$$

Similarly,  $P_g$  is derived as:

$$P_g = \frac{3}{2} \times \begin{bmatrix} u_{sd+}^+ i_{gd+}^+ + u_{sd-}^- i_{gd-}^- + u_{gq+}^+ i_{gq+}^+ + u_{gq-}^- i_{gq-}^- \\ \left( u_{sd-}^- i_{gd+}^+ + u_{sq-}^- i_{gq+}^+ + \right. \\ \left. u_{sd+}^+ i_{gd-}^- + u_{sq+}^+ i_{gq-}^- \right) \times \cos(\theta^+ + \theta^-) + \\ \left( u_{sq-}^- i_{gd+}^+ - u_{sd-}^- i_{gq+}^+ - \right. \\ \left. u_{sq+}^+ i_{gd-}^- + u_{sd+}^+ i_{gq-}^- \right) \times \sin(\theta^+ + \theta^-) \end{bmatrix} \quad (11)$$

where  $u_s$  is equal to  $u_g$ .

Combined with (7)–(11), equation (6) can be expressed as:

$$\begin{aligned} \frac{2CL_s U_{dc}}{3L_m} \frac{dU_{dc}}{dt} = & \left( u_{sd+}^+ + u_{sd-}^- \cos(\theta^+ + \theta^-) - \right. \\ & \left. u_{sq-}^- \sin(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gd+}^{+*} - s_1 i_{rd+}^{+*} \right) + \\ & \left( u_{sq+}^+ + u_{sq-}^- \cos(\theta^+ + \theta^-) - \right. \\ & \left. u_{sd-}^- \sin(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gq+}^{+*} - \left( i_{rq+}^+ + \frac{|\mathbf{U}_s^+|}{L_m \omega_{pll}^+} \right) s_1 \right) + \\ & \left( u_{sd-}^- + u_{sd+}^+ \cos(\theta^+ + \theta^-) - \right. \\ & \left. u_{sq+}^+ \sin(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gd-}^{+*} - s_2 i_{rd-}^{+*} \right) + \\ & \left( u_{sq-}^- + u_{sq+}^+ \cos(\theta^+ + \theta^-) + \right. \\ & \left. u_{sd+}^+ \sin(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gq-}^{+*} - \left( i_{rq-}^- + \frac{|\mathbf{U}_s^-|}{L_m \omega_{pll}^-} \right) s_2 \right) \end{aligned} \quad (12)$$

Considering that the presence of the integrator limits the transient performance of the PLL-based DFIG system during grid faults [20], [21],  $i_{gd}^{+*}$  can be obtained through proportional control of the DCV deviation [11], described as:

$$i_{gd+}^{+*} = k_{dc} (U_{dc}^* - U_{dc}) \quad (13)$$

Substituting (13) into (12) yields:

$$\begin{cases}
 J_c \frac{dU_{dc}}{dt} = -P_D + P_{so} - P_{do} \\
 J_c = \frac{2}{3} \frac{L_s}{L_m} C U_{dc} \\
 P_D = D_c \times (U_{dc} - U_{dc}^*) \\
 D_c = \frac{L_s}{L_m} k_{dc} (u_{sd+}^+ + u_{sd-}^- \cos(\theta^+ + \theta^-)) \\
 P_{so} = (u_{sd-}^- + u_{sd+}^+ \cos(\theta^+ + \theta^-)) \times \left( \frac{L_s}{L_m} i_{gd-}^{*-} - s_2 i_{rd-}^{*-} \right) + \\
 \left( u_{sd+}^+ \sin(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gq-}^{*-} - \left( i_{rq-}^{*-} + \frac{|U_s^-|}{L_m \omega_{pll}^-} \right) s_2 \right) - \\
 \left( u_{sd+}^+ + u_{sd-}^- \cos(\theta^+ + \theta^-) \right) \times s_1 i_{rd+}^{+*} - \\
 \left( u_{sd-}^- \sin(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gq+}^{+*} - \left( i_{rq+}^{+*} + \frac{|U_s^+|}{L_m \omega_{pll}^+} \right) s_1 \right) \\
 P_{do} = (u_{sq-}^- + u_{sq+}^+ \cos(\theta^+ + \theta^-)) \times \left( \frac{L_s}{L_m} i_{gq-}^{*-} - \left( i_{rq-}^{*-} + \frac{|U_s^-|}{L_m \omega_{pll}^-} \right) s_2 \right) + \\
 u_{sq-}^- \sin(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gd+}^{+*} - s_1 i_{rd+}^{+*} \right) + \\
 \left( u_{sq+}^+ + u_{sq-}^- \cos(\theta^+ + \theta^-) \right) \times \left( \frac{L_s}{L_m} i_{gq+}^{+*} - \left( i_{rq+}^{+*} + \frac{|U_s^+|}{L_m \omega_{pll}^+} \right) s_1 \right) - \\
 u_{sq+}^+ \sin(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gd-}^{*-} - s_2 i_{rd-}^{*-} \right)
 \end{cases} \quad (14)$$

where  $P_D$  is the equivalent damping power;  $P_{so}$  is the equivalent static stable power; and  $P_{do}$  is the equivalent dynamic stable power;  $J_c$  denotes the equivalent inertia coefficient; and  $D_c$  denotes the equivalent damping coefficient of the DCV.

In (14),  $P_{do}$  represents the influence of the PLL control characteristics on DCV transient response, while  $P_D$  and  $P_{so}$  denote the influence of the power coordination control characteristics between the RSC and GSC on DCV transient behavior. In addition, equation (14) shows that the DCV transient characteristics are also influenced by the control parameter  $k_{dc}$ , the DC-link capacitance  $C$ ,  $s_1$ , and  $s_2$ . Accordingly, the phase trajectories of the DCV under different control parameters are illustrated in Figs. 3 and 4, where  $A^+$  and  $A^-$  are the initial points of the system.

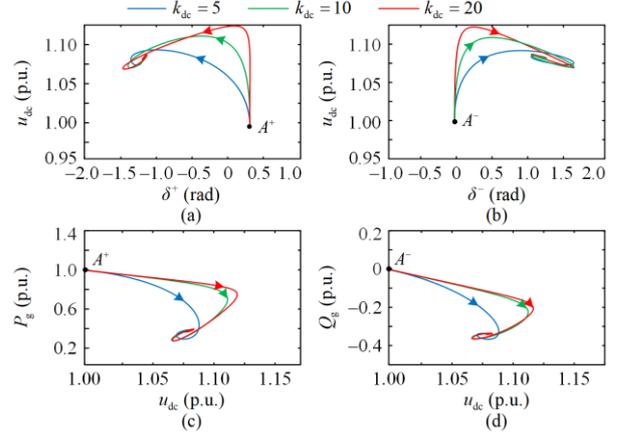


Fig. 3. Influence of the proportional coefficient in the DCV control loop. (a) Phase trajectories of DCV and PS equivalent power angle. (b) Phase trajectories of DCV and negative sequence (NS) equivalent power angle. (c) Phase trajectories of DCV and active power. (d) Phase trajectories of DCV and reactive power.

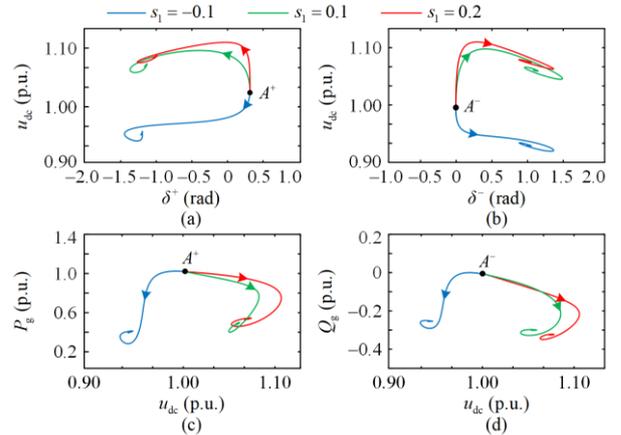


Fig. 4. Influence of  $s_1$ . (a) Phase trajectories of DCV and PS equivalent power angle. (b) Phase trajectories of DCV and NS equivalent power angle. (c) Phase trajectories of DCV and active power. (d) Phase trajectories of DCV and reactive power.

In Figs. 3 and 4, the LVRT current references of the RSC and GSC are 0.15 p.u., -0.35 p.u., -0.05 p.u., -0.45 p.u., 0, 0, and 0. The results show that although the DCV transient characteristics are significantly influenced by  $k_{dc}$  and  $s_1$ , the DCV cannot be restored to  $U_{dc0}$  solely by adjusting the system control parameters. Furthermore, the dynamic behavior of the DCV has a considerable impact on the power output of the DFIG system, which cannot be overlooked.

Figure 5 illustrates the phase trajectories diagram of DCV under different DC-link capacitance values. Although increasing the DC-link capacitance allows the DCV to reach a transiently stable state more quickly, the DCV cannot be restored to  $U_{dc0}$  simply by adjusting the capacitance value. Similarly, as demonstrated in Figs. 6–9, by increasing the proportional coefficient or decreasing the integration coefficient of the PNS PLL, the PNS equivalent power angles, and the DCV can return to

transiently stable states more quickly, yet the DCV still cannot be restored to  $U_{dc0}$ . Therefore, it is essential to further analyze the transient behavior of the DCV during asymmetrical faults, identify the dominant factors that influence the TS of the DCV, and study the control strategy aimed at enhancing the TS of the DCV.

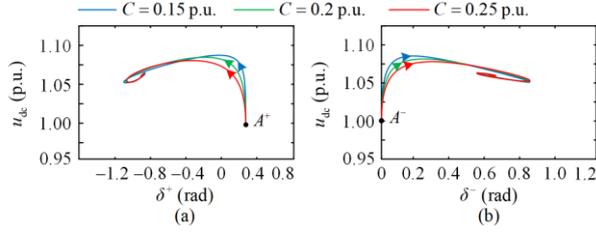


Fig. 5. Influence of DC-link capacitance. (a) Phase trajectories of DCV and PS equivalent power angle. (b) Phase trajectories of DCV and NS equivalent power angle.

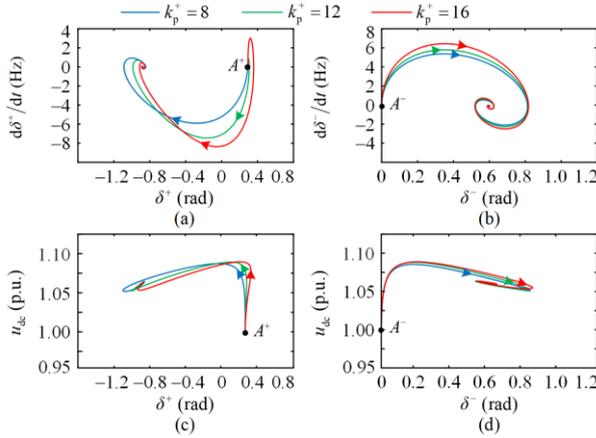


Fig. 6. Influence of the proportional coefficient of PS PLL. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

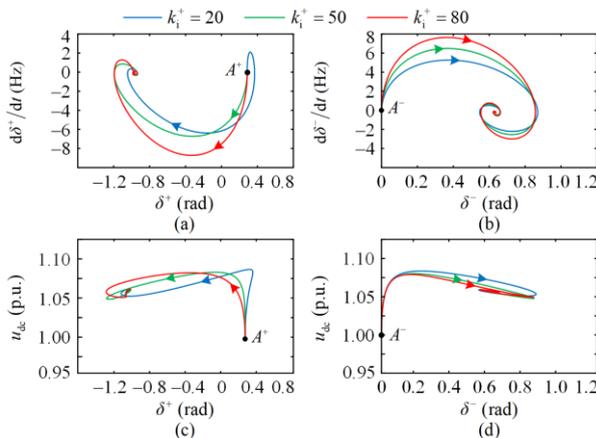


Fig. 7. Influence of the integration coefficient of PS PLL. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

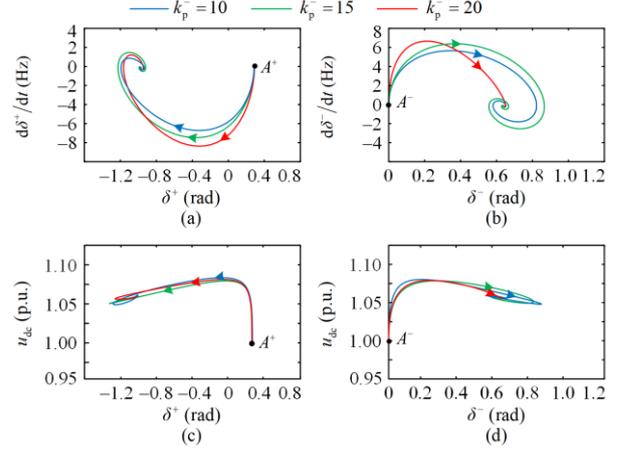


Fig. 8. Influence of the proportional coefficient of NS PLL. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

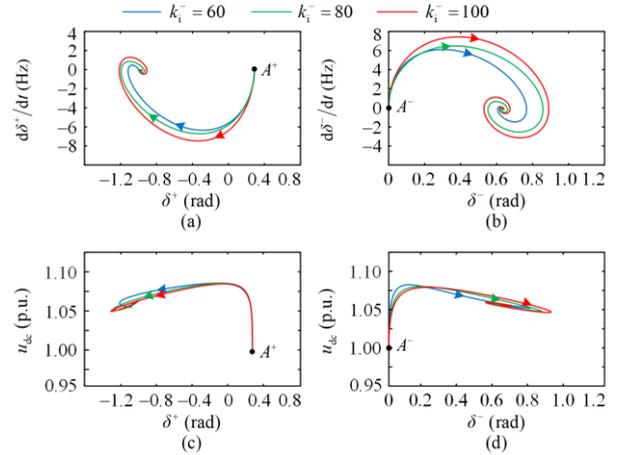


Fig. 9. Influence of the integration coefficient of the NS PLL. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

### III. ANALYSIS OF TRANSIENT STABILITY IN THE DCV CONTROL TIME SCALE

Combining (1)–(14), the dynamic DCV control block diagram under asymmetrical faults can be obtained as shown in Fig. 10. The TS of DCV in the DFIG system is primarily influenced by the control characteristics of PLL, the coupling characteristics between the PNS components, and the coordinated power control between the RSC and GSC on the TS of DCV. Therefore, the mechanism of how these factors affecting the TS process and stability level of DCV will be further analyzed in this section.

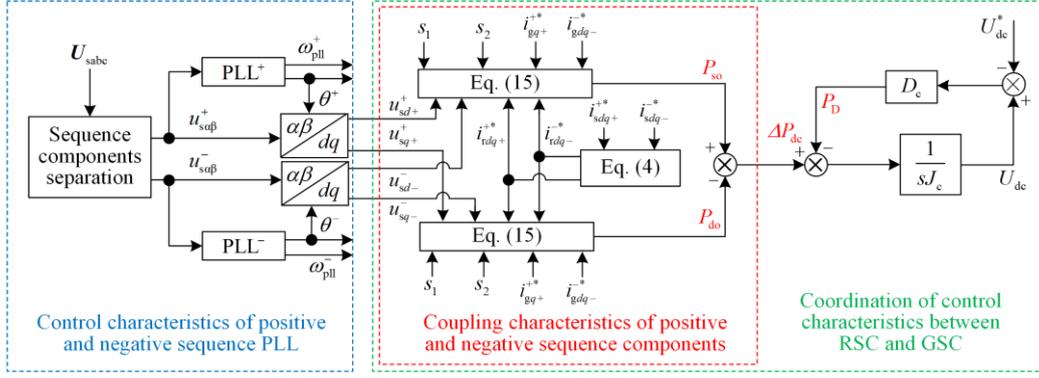


Fig. 10. Dynamic control block diagram of the DCV during asymmetrical faults.

### A. Transient Stability Process of the DCV

According to Fig. 10, when an asymmetrical grid short-circuit fault occurs, the DFIG system switches to the LVRT control mode and adopts the single current loop control strategy. This results in a significant change in the output angular frequency of the PLL, which further leads to fluctuation in  $u_{sdq+}^+$  and  $u_{sdq-}^-$ . In this situation, based on (14),  $P_{do}$  is no longer equal to 0, and consequently, the power balance of the DC-link capacitance is disrupted, causing  $U_{dc}$  to deviate from  $U_{dc}^*$ . At the same time, the PLL suppresses the fluctuation of  $u_{sq+}^+$  and  $u_{sq-}^-$  to eliminate the interference  $P_{D}$ .

In addition, under the influence of  $\Delta U_{dc}$ , a signal will be generated by the DCV control loop to compensate for the unbalanced power caused by  $P_{so}$ , and prevent  $U_{dc}$  from diverging from  $U_{dc}^*$ . Ultimately, the DCV can be restored to a new stable state provided that the equilibrium point of the DFIG system exists.

From the above analysis,  $D_c$  acts in a manner similar to damping by preventing variation in DCV, while  $J_c$  influences the rate of change of DCV and can be regarded as the inertia coefficient. Therefore, the transient process of DCV in the DFIG system is comparable to that of a SG. Furthermore, as shown in Fig. 10, the PNS PLL mainly influences the power balance of the DC capacitor by regulating the terminal voltages  $u_{sdq+}^+$  and  $u_{sdq-}^-$ . The RSC and GSC primarily affect the power balance of the DC-link capacitor by controlling the output PNS currents.

### B. Transient Stability Mechanism of the DCV

Based on the above analysis, if  $u_{sq+}^+$  and  $u_{sq-}^-$  cannot be fully regulated to 0 by the PLL, the disturbance of  $P_{do}$  will not be removed, thereby making the DCV unstable. Therefore, the main factors that cause instability of the DCV in the system can be classified as follows.

#### 1) The Disturbance of $P_{do}$ Cannot Be Eliminated

From Fig. 10 and (14), if  $u_{sq+}^+$  and  $u_{sq-}^-$  cannot be completely regulated to 0 by the PLL, the DCV will be unstable. Furthermore, since the PLL control characteristics are closely related to the dynamic behavior of the PNS equivalent power angles  $\delta^+$  and  $\delta^-$ , in the system, the DCV transient characteristics are also affected by the dynamic behavior of the PNS equivalent power angles. Therefore, it is necessary to analyze the relationship between the dynamic behaviors of the equivalent power angles and the transient process of DCV during asymmetrical grid faults. Equation (14) can be further written as:

$$\left\{ \begin{aligned} & \frac{2CU_{dc}L_s}{3L_m} \times \frac{dU_{dc}}{dt} = -D_c \times (U_{dc} - U_{dc}^*) + P_{so+} - P_{do+} + P_{so-} - P_{do-} \\ & P_{so+} = -u_{sd+}^+ s_1 i_{rd+}^{+*} + u_{sd+}^+ \cos(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gd-}^{*-} - s_2 i_{rd-}^{*-} \right) + \\ & \quad u_{sd+}^+ \sin(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gq-}^{*-} - \left( i_{rq-}^{*-} + \frac{|U_s^-|}{L_m \omega_{pll}^-} \right) s_2 \right) \\ & P_{so-} = u_{sd-}^- \left( \frac{L_s}{L_m} i_{gd-}^{*-} - s_2 i_{rd-}^{*-} \right) - u_{sd-}^- \cos(\theta^+ + \theta^-) s_1 i_{rd+}^{+*} - \\ & \quad u_{sd-}^- \sin(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gq+}^{+*} - i_{rq+}^{+*} - \frac{|U_s^+|}{L_m \omega_{pll}^+} \right) s_1 \\ & P_{do+} = u_{sq+}^+ \left( \frac{L_s}{L_m} i_{gq+}^{+*} - \left( i_{rq+}^{+*} + \frac{|U_s^+|}{L_m \omega_{pll}^+} \right) s_1 \right) - \\ & \quad u_{sq+}^+ \sin(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gd-}^{*-} - s_2 i_{rd-}^{*-} \right) + \\ & \quad u_{sq+}^+ \cos(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gq-}^{*-} - \left( i_{rq-}^{*-} + \frac{|U_s^-|}{L_m \omega_{pll}^-} \right) s_2 \right) \\ & P_{do-} = u_{sq-}^- \left( \frac{L_s}{L_m} i_{gq-}^{*-} - \left( i_{rq-}^{*-} + \frac{|U_s^-|}{L_m \omega_{pll}^-} \right) s_2 \right) + \\ & \quad u_{sq-}^- \sin(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gd+}^{+*} - s_1 i_{rd+}^{+*} \right) + \\ & \quad u_{sq-}^- \cos(\theta^+ + \theta^-) \times \left( \frac{L_s}{L_m} i_{gq+}^{+*} - \left( i_{rq+}^{+*} + \frac{|U_s^+|}{L_m \omega_{pll}^+} \right) s_1 \right) \end{aligned} \right. \quad (15)$$

Multiplying (15) by  $dt$  yields:

$$\left\{ \begin{aligned} \frac{2CU_{dc}L_s}{3L_m} dU_{dc} &= -D_c \times (U_{dc} - U_{dc}^*) dt + \\ & (P_{so+} - P_{do+} + P_{so-} - P_{do-}) dt \end{aligned} \right. \quad (16)$$

Since  $\delta^+$  and  $\delta^-$  can be expressed as [23]:

$$\left\{ \begin{aligned} \frac{d\delta^+}{dt} &= \omega_b (\omega_{pll}^+ - \omega_g) \\ \frac{d\delta^-}{dt} &= \omega_b (\omega_{pll}^- + \omega_g) \end{aligned} \right. \quad (17)$$

Substitution of (17) into (16), followed by integration, yields:

$$\begin{aligned} (\omega_{pll}^- + \omega_g)(\omega_{pll}^+ - \omega_g) \frac{\omega_b CL_s}{3L_m} (U_{dc}^2 - U_{dc0}^2) + (\omega_{pll}^- + \omega_g) \times \\ \int_{\delta_0^+}^{\delta^+} D_c (U_{dc} - U_{dc}^*) d\delta^+ = (\omega_{pll}^- + \omega_g) \int_{\delta_0^+}^{\delta^+} (P_{so+} - P_{do+}) d\delta^+ + \Delta E_D \\ (\omega_{pll}^+ - \omega_g) \int_{\delta_0^-}^{\delta^-} (P_{so-} - P_{do-}) d\delta^- \end{aligned} \quad (18)$$

where  $\Delta E_K$  represents the equivalent kinetic energy of the DC-link capacitor during the transient process of DCV;  $U_{dc0}$  is the initial voltage of  $U_{dc}$ ;  $\Delta E_{p+}$  and  $\Delta E_{p-}$  denote the equivalent potential energy generated by the PS and NS operating points during asymmetrical faults, respectively; and  $\Delta E_D$  is the equivalent damping energy consumed by the DC-link capacitor.

According to the dynamic behaviors of  $\delta^+$  and  $\delta^-$  [11], two transient instability types of DCV can occur. When  $\delta^+$  or  $\delta^-$  increases monotonically during the faults,  $P_{do+}$  and  $P_{do-}$  will always exist. In this case, if  $\Delta E < 0$  ( $\Delta E = \Delta E_{p+} + \Delta E_{p-} - \Delta E_D$ ), the DC-link capacitor releases kinetic energy to compensate for  $\Delta E$ , which results in a continuous decrease of DCV. If  $\Delta E > 0$ , the DC-link capacitor reduces the excessive  $\Delta E$  by increasing kinetic energy, leading to a continuous rise of DC voltage. However, when  $\delta^+$  or  $\delta^-$  decreases monotonically, if  $\Delta E < 0$ , the DC capacitor absorbs the excessive  $\Delta E$ , which results in a continuous increase of DCV. Conversely, when  $\Delta E > 0$ , the DC-link capacitor releases kinetic energy to compensate for  $\Delta E$ , which leads to a continuous decrease of DCV.

Based on the above analysis, the DCV can maintain TS only when  $P_{do}$  is eliminated. Therefore, the necessary condition for the DCV to maintain TS during

asymmetrical grid faults is that both  $u_{sq+}^+$  and  $u_{sq-}^-$  are effectively regulated to 0, which requires the PS PLL and NS PLL to accurately track the PNS grid voltage of the DFIG system [23].

## 2) The Unbalanced Power Between $P_{so}$ and $P_D$ Cannot Be Eliminated

As illustrated in Fig. 10, although the existence of an equilibrium point in the DFIG system is the prerequisite for the DCV to remain TS during the asymmetrical grid faults, the TS level of the DCV is also affected by the power coordinated control characteristics between the RSC and GSC. Therefore, it is necessary to further investigate the influencing mechanism of the power coordinated control characteristics between the RSC and GSC on the TS level of the DCV.

Integrating both sides of (16) yields:

$$\begin{aligned} \frac{L_s C}{3L_m} (U_{dc}^2 - U_{dc0}^2) + \int_{t_0}^{t_1} D_c (U_{dc} - U_{dc}^*) dt = \\ \int_{t_0}^{t_1} (P_{g,so} - P_{r,so}) dt - \int_{t_0}^{t_1} P_{do} dt \end{aligned} \quad (19)$$

where  $t_0$  is the initial moment of the fault; and  $t_1$  is the moment when the fault recovers;  $\Delta W_C$  denotes the energy accumulated or released by the DC capacitor during asymmetrical grid faults;  $\Delta W_D$ ,  $\Delta W_s$ , and  $\Delta W_d$  represent the energy produced by  $P_D$ ,  $P_{so}$ , and  $P_{do}$ , respectively.

From (14) and (19), under the condition that  $u_{sq+}^+$  and  $u_{sq-}^-$  can be controlled to 0,  $P_{do}$  will remain 0 during asymmetrical grid faults, and no energy conversion caused by  $P_{do}$  will occur. Moreover, according to [11], if  $u_{sq+}^+$  and  $u_{sq-}^-$  can be controlled to 0, this implies that the equilibrium point of the system exists, and the negative  $D_c$  will not lead to DCV instability. Therefore, the DCV TS level during asymmetrical faults primarily depends on  $\Delta W_s$ . By analyzing the interaction between  $P_{g,so}$  and  $P_{r,so}$ , the following two DCV TS levels can be identified.

**Case 1:** When  $P_{g,so}$  is greater than  $P_{r,so}$ , the energy absorbed by the GSC exceeds the energy released by the RSC during the faults, resulting in  $\Delta W_s > 0$ . In this case, the DC capacitor will be charged to balance the unbalanced energy  $\Delta W_s$  which causes  $U_{dc}$  to be greater

than  $U_{dc0}$  during asymmetrical grid faults. Throughout this process,  $\Delta W_D$  always remains greater than 0.

**Case 2:** During asymmetrical grid faults, if  $P_{g\_so}$  is less than  $P_{r\_so}$ ,  $\Delta W_s$  will be less than 0. In this case, the DC-link capacitor will discharge energy to offset the imbalance between the RSC and GSC, which leads to  $U_{dc}$  being less than  $U_{dc0}$ . Similarly, in this process,  $\Delta W_D$  always remains greater than 0.

Based on the above analysis, the transient instability of the DCV in the DFIG system is primarily determined by the control characteristics of PLL during asymmetrical grid faults, while the DCV TS level is mainly influenced by the power coordinated control characteristics between the RSC and GSC.

#### IV. TRANSIENT STABILITY COORDINATED CONTROL STRATEGY OF THE DCV

According to the analysis in Section III, the necessary condition for the DCV to remain in TS during asymmetrical grid faults is that  $u_{sq+}^+$  and  $u_{sq-}^-$  can be controlled to 0. However, if this condition is satisfied, the unbalanced power coordinated control between the RSC and GSC can still affect the TS of the DCV. Therefore, to improve the DCV TS level and the TS of the DFIG, this section investigates the influence of different RSC active and reactive currents on the transient characteristics of the PNS equivalent power angles and DCV. Figures 11–14 illustrate the phase trajectories diagram of the DCV under different rotor PNS active and reactive current references.

As observed in Figs. 11–14, although the DCV TS level is not influenced by the PNS reactive current output by the RSC, the transient component of the DCV and the PS equivalent power angle increase progressively with the rise of the output PNS reactive current. This weakens the TS margin of the PS power angle and deteriorates the transient characteristics of DCV.

Furthermore, while a moderate increase in the PNS active current output by the RSC causes the DCV to exhibit different TS levels, the TS margin of the PNS equivalent power angle gradually improves, which enhances the overall TS margin of the system. However, an excessive PNS active current will reduce the TS margin of the system and may even result in system instability. Therefore, it is essential to properly coordinate the PNS active and reactive current of output by the RSC and GSC during asymmetrical grid faults. Otherwise, transient instability of the DCV and the system can occur.

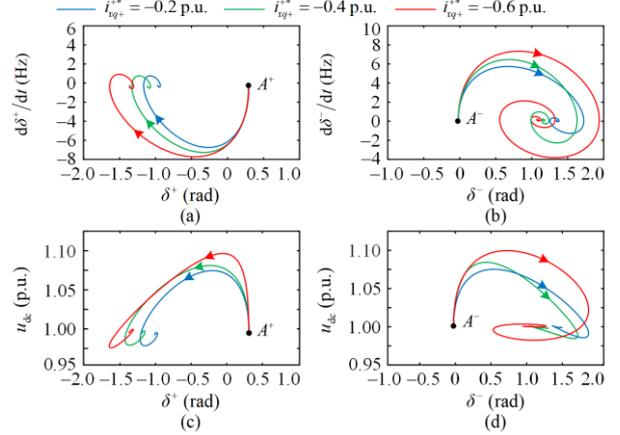


Fig. 11. Influence of rotor PS reactive current references. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

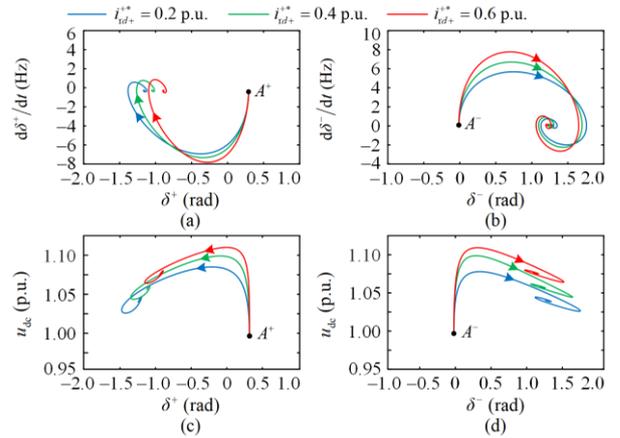


Fig. 12. Influence of rotor PS active current references. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

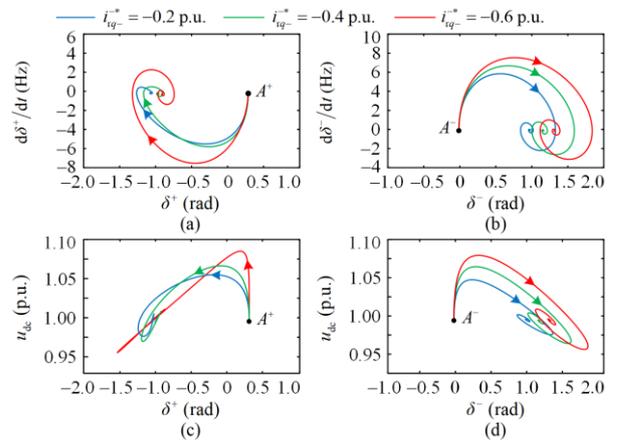


Fig. 13. Influence of rotor NS reactive current references. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) Phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

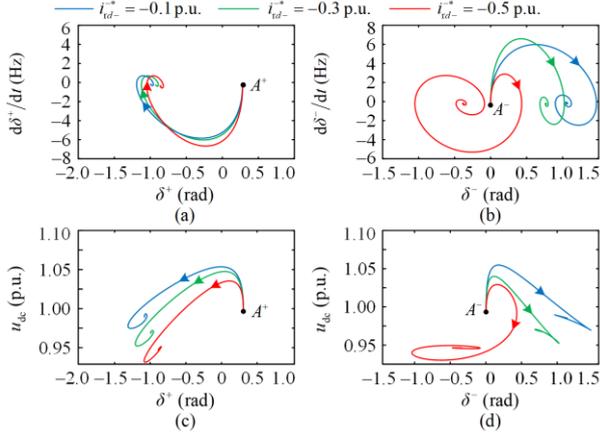


Fig. 14. Influence of rotor NS active current references. (a) Phase trajectories diagram of  $\delta^+$  and  $d\delta^+/dt$ . (b) Phase trajectories of  $\delta^-$  and  $d\delta^-/dt$ . (c) phase trajectories of DCV and PS equivalent power angle. (d) Phase trajectories of DCV and NS equivalent power angle.

Considering the requirement of the latest grid codes [18], the output PNS reactive power currents  $i_{q+}^+$  and  $i_{q-}^-$  during asymmetrical grid faults must satisfy the followings:

$$\begin{cases} i_{q+}^+ = -K^+(0.9 - U_s^+)I_N, & 0.6 \leq U_s^+ \leq 0.9 \\ i_{q-}^- = -K^-U_s^-I_N \end{cases} \quad (20)$$

where  $K^+$  is the PS reactive current proportional coefficient of the REPF;  $K^-$  is the NS reactive current proportional coefficient of the REPF; and  $I_N$  is the rated current of the REPF.

According to (14), to restore the DCV to a stable state, the equivalent damping power  $P_D$  must compensate for the fluctuations generated by the static stable power  $P_{so}$ . Since  $P_D$  is produced by  $\Delta U_{dc}$ , while neither  $u_{sd+}^+$  nor  $u_{sd-}^-$  equals 0 during asymmetrical grid faults, the DCV fluctuation caused by  $P_{so}$  can only be eliminated by coordinating LVRT current references ( $i_{rdq+}^{+*}$ ,  $i_{rdq-}^{-*}$ ,  $i_{gdq+}^{+*}$ , and  $i_{gdq-}^{-*}$ ) of the RSC and GSC. Therefore, the following equation can be obtained:

$$\begin{cases} \frac{L_s}{L_m} k_{dc} (U_{dc}^* - U_{dc}) + \frac{L_s}{L_m} i_{gd-}^{-*} - s_1 i_{rd+}^{+*} - s_2 i_{rd-}^{-*} = 0 \\ \frac{L_s}{L_m} i_{gg+}^{+*} - s_1 \left( i_{rq+}^{+*} + \frac{U_s^+}{L_m \omega_{pll}} \right) = 0 \\ \frac{L_s}{L_m} i_{gg-}^{-*} - s_2 \left( i_{rq-}^{-*} + \frac{U_s^-}{L_m \omega_{pll}} \right) = 0 \end{cases} \quad (21)$$

As a result, the coordinated control method of the PNS reactive currents for the RSC and GSC under asymmetrical grid faults is expressed as:

$$\begin{cases} i_{rq+}^{+*} = \frac{K^+(0.9 - U_s^+)I_N L_s}{L_m(1+s_1)} - \frac{U_s^+}{L_m \omega_{pll}^+}, & 0.6 \leq U_s^+ \leq 0.9 \\ i_{rq-}^{-*} = \frac{K^-U_s^-I_N L_s}{L_m(1+s_2)} - \frac{U_s^-}{L_m \omega_{pll}^-} \\ i_{gg+}^{+*} = \frac{s_1 K^+(0.9 - U_s^+)I_N}{1+s_1} \\ i_{gg-}^{-*} = \frac{s_2 K^-U_s^-I_N}{1+s_2} \end{cases} \quad (22)$$

Considering that the grid codes [21]–[23] do not require the NS active current ( $i_{d-}^-$ ) for the REPF,  $i_{d-}^-$  is set to 0 in this paper.

By substituting (22) into (19), the PNS active current under asymmetrical faults can be obtained on the condition that the equilibrium point of the system exists. In this situation, the optimal distribution strategy of the PNS active currents for the RSC and GSC during asymmetrical faults can be expressed as:

$$\begin{cases} i_{rd+}^{+*} = -\frac{L_s i_{d+}^{+*}}{L_m(1+s_1)} \\ i_{gd+}^{+*} = k_{dc}(U_{dc}^* - U_{dc}) + s_1 i_{rd+}^{+*} \\ i_{rd-}^{-*} = -\frac{L_s i_{d-}^{-*}}{L_m(1+s_2)} \\ i_{gd-}^{-*} = -\frac{s_2 i_{d-}^{-*}}{1+s_2} \end{cases} \quad (23)$$

Since the rotor may experience overvoltage or overcurrent during faults, the rotor current reference values should meet the constraints [27] given as:

$$\begin{cases} i_{r\_max} = \max \left( \sqrt{(i_{r+}^+)^2 + (i_{r-}^-)^2} + 2i_{r+}^+ i_{r-}^- \cos(\delta^+ - \delta^- - \theta^+ - \theta^-) \right) \\ i_{r+}^+ = \sqrt{(i_{rd+}^+)^2 + (i_{rq+}^+)^2} \\ \theta^+ = \tan^{-1}(i_{rq+}^+ / i_{rd+}^+) \\ i_{r-}^- = \sqrt{(i_{rd-}^-)^2 + (i_{rq-}^-)^2} \\ \theta^- = \tan^{-1}(i_{rq-}^- / i_{rd-}^-) \end{cases} \quad (24)$$

If the proposed optimal PNS active and reactive currents distribution strategy does not satisfy (25), it further limits the NS voltage components of the rotor during asymmetrical grid faults. To eliminate the NS components in the stator flux and to prevent rotor overvoltage [28], the NS rotor current should be set as:

$$\begin{cases} i_{rd-}^{*-} = -\frac{L_m u_{sq-}^-}{\sigma L_s L_r \omega_{pll}^-} \\ i_{rq-}^{*-} = -\frac{L_m u_{sd-}^-}{\omega_{pll}^- \sigma L_s L_r} \end{cases} \quad (25)$$

where  $\sigma = 1 - L_m^2 / L_s L_r$ .

Under these conditions, the coordinated distribution strategy of the PNS active and reactive currents between the RSC and GSC should be modified as:

$$\begin{cases} i_{rq+}^{+*} = \frac{K^+ (0.9 - U_s^+) I_N L_s}{L_m (1 + s_1)} - \frac{U_s^+}{L_m \omega_{pll}^+}, \quad 0.6 \leq U_s^+ \leq 0.9 \\ i_{gq+}^{+*} = \frac{s_1 K^+ (0.9 - U_s^+) I_N}{1 + s_1} \\ i_{gd+}^{+*} = k_{dc} (U_{dc}^* - U_{dc}) \\ i_{rq-}^{*-} = -\frac{L_m U_s^-}{\omega_{pll}^- \sigma L_s L_r} \\ i_{gq-}^{*-} = -\frac{s_2 U_s^-}{L_s \omega_{pll}^-} \left( 1 + \frac{L_m^2}{L_r L_s \sigma} \right) \\ i_{rd+}^{+*} = \frac{k_{dc} L_s}{s_1 L_m} (U_{dc}^* - U_{dc}) \\ i_{rd-}^{*-} = 0 \\ i_{gd-}^{*-} = 0 \end{cases} \quad (26)$$

It should be noted that under different asymmetrical grid fault conditions, the corresponding optimal PNS active and reactive currents of the RSC and GSC for the DFIG system can be automatically calculated, provided that the grid fault information required in (19)–(26) is available. Therefore, the proposed strategy is applicable to different types of asymmetrical grid faults. Furthermore, according to the energy equation in (18), the variation boundaries of the DCV can be determined when the equilibrium point of the system does not exist, while under the condition that the equilibrium point of the DFIG system exists, the variation boundaries of the DCV can be obtained using (19). Therefore, the variation boundaries of the DCV for the DFIG system during asymmetrical grid faults can be determined.

## V. SIMULATION AND EXPERIMENT VALIDATION

### A. Simulation Validation

A simulation model is built in MATLAB/Simulink to verify the correctness of the above analysis and the proposed coordinated distribution strategy. Figure 1 illustrates the simulated system configuration, while Table AI in Appendix A provides the detailed parameters of the system, with the transmission line parameters converted to 690 V low-voltage side. Before the occurrence of the fault, the DFIG system operates in MPPT mode, and the DCV is controlled using the PI control strategy. On the AC grid side, a

two-phase-to-ground short-circuit fault occurs at 1.6 s. At 1.7 s, the RSC is instantly blocked and adopts a single current loop control strategy. At the same time, the integrator in the DCV control loop is blocked. In Fig. 15, the LVRT current references of the RSC and GSC are assigned according to the values presented in the cases of Table AII in Appendix.

Figure 15 presents the simulation case of the instability phenomenon for the DCV caused by the loss of synchronism of the PNS PLL during angular frequency rise. When  $U_{fab}$  decreases to 0.15 p.u., and the current references of the RSC and GSC are set accordingly to Case 1 in Table AII,  $u_{sq+}^+$  and  $u_{sq-}^-$  remain greater than 0. Consequently, both  $P_{do+}$  and  $P_{do-}$  continuously exist, resulting in DC voltage instability. At the same time, as shown in Figs. 15(c) and (f), the output active and reactive power of the DFIG stator and rotor also become unstable.

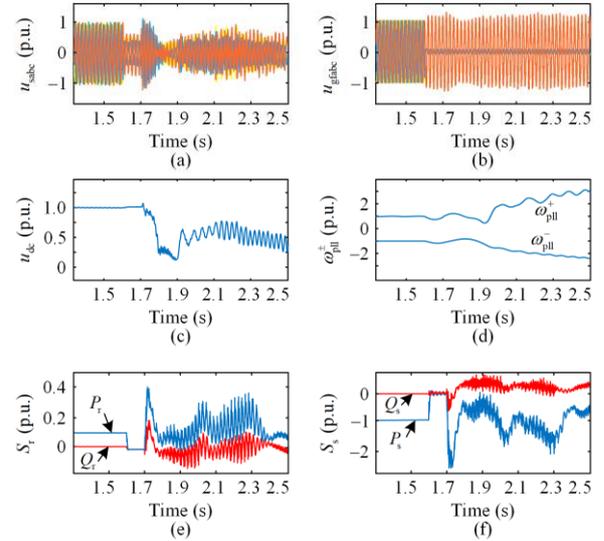


Fig. 15. Case 1: instability of the DCV caused by the lack of an equilibrium point. (a) Three-phase stator voltage of the system. (b) Three-phase fault voltage  $U_{gfabc}$ . (c) DCV of the DFIG system. (d) Output frequency of the PNS PLL. (e) Output power of the rotor  $P_r$ . (f) Output power of the stator  $P_s$ .

Figures 16–18 display the simulation results of the DFIG system when  $U_{fab}$  decreases to 0.6 p.u., 0.4 p.u., and 0.2 p.u., respectively. As seen, when the LVRT current references of the RSC and GSC are configured according to Cases 2 to 4 in Table AII, the disturbances of  $P_{do}$  and  $P_{so}$  can be effectively eliminated using the proposed strategy. As a result, the DCV is rapidly restored to a new stable condition. This demonstrates that the proposed coordinated control strategy can significantly enhance the DCV TS under various levels of grid voltage sags. The simulation outcomes align well with the previous analysis.

Stage 1: conventional positive and negative sequence reactive support strategy  
 Stage 2: proposed positive and negative sequence active and reactive support strategy

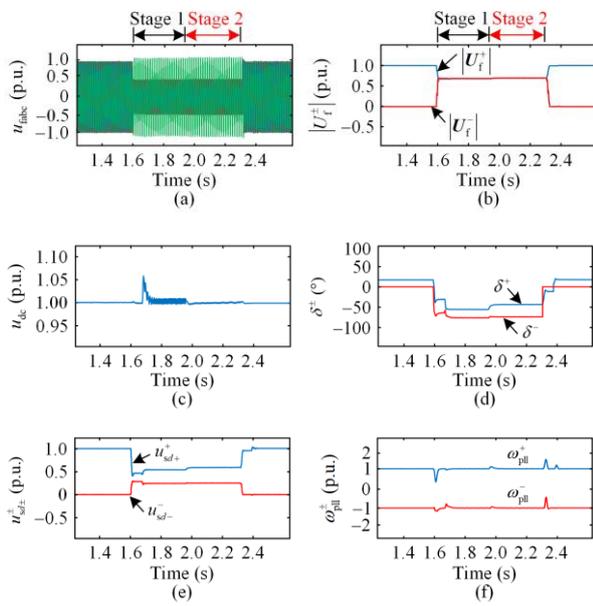


Fig. 16. Case 2: simulation results of the DFIG system when  $U_{fab}$  drops to 0.6 p.u. (a) Three-phase voltage of the fault point. (b) Amplitude of the PNS fault voltage. (c) DCV of the DFIG system. (d) PNS equivalent power angles. (e) PNS  $d$ -axis voltage components. (f) Output frequency of the PNS PLL.

Stage 1: conventional positive and negative sequence reactive support strategy  
 Stage 2: proposed positive and negative sequence active and reactive support strategy

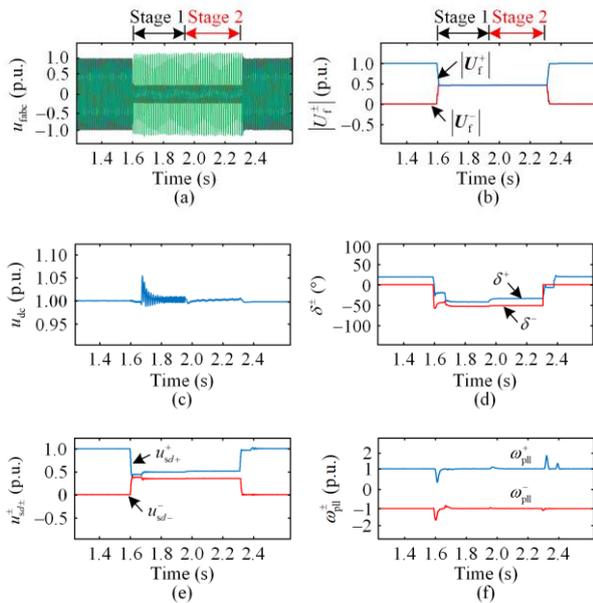


Fig. 17. Case 3: simulation results of the DFIG system when  $U_{fab}$  drops to 0.4 p.u. (a) Three-phase voltage at the fault point. (b) Amplitude of the PNS fault voltage. (c) DCV of the DFIG system. (d) PNS equivalent power angles. (e) PNS  $d$ -axis voltage components. (f) Output frequency of the PNS PLL.

Stage 1: conventional positive and negative sequence reactive support strategy  
 Stage 2: proposed positive and negative sequence active and reactive support strategy

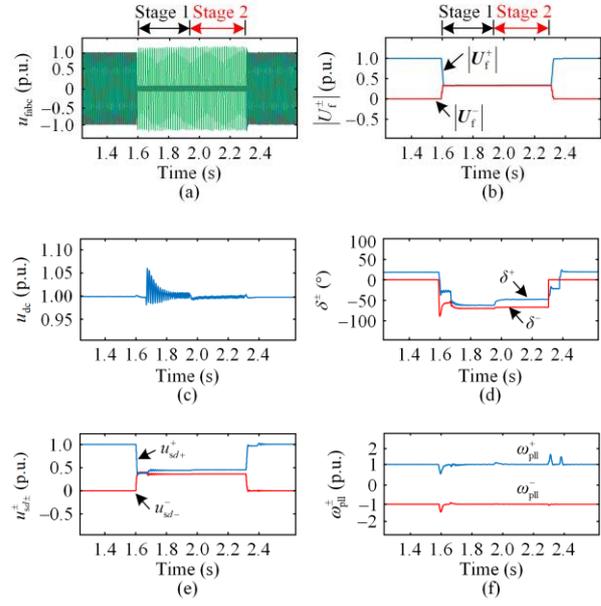
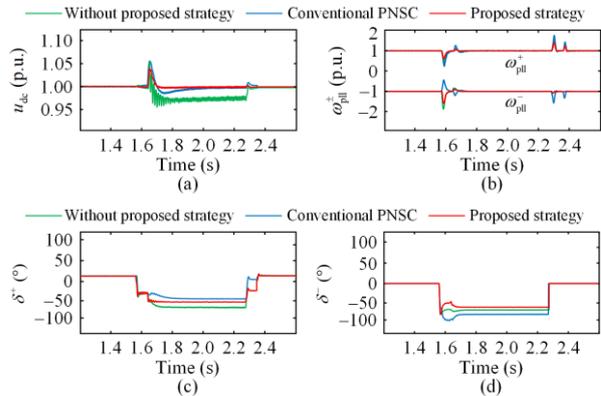


Fig. 18. Case 4: simulation results of the DFIG system when  $U_{fab}$  drops to 0.2 p.u. (a) Three-phase voltage at the fault point. (b) Amplitude of the PNS fault voltage. (c) DCV of the DFIG system. (d) PNS equivalent power angles. (e) PNS  $d$ -axis voltage components. (f) Output frequency of the PNS PLL.

Figure 19 compares the simulation results under different control strategies. When  $U_{fab}$  decreases to 0.4 p.u., the DCV deviates from  $U_{dc0}$  without applying the proposed strategy during asymmetrical grid faults. Moreover, when the conventional strategy based on PNSC [28] is used, although the DCV can still be controlled to  $U_{dc0}$ , both the DCV and the output frequency of the PLL reach the new stable conditions more rapidly with the proposed strategy. In addition, compared with the method in [28], the PNS equivalent power angles remain closer to 0, which applies a greater system TS margin. Therefore, the coordinated current control strategy proposed in this paper can enhance the DCV TS and reduce the risk of transient instability in the DFIG system.



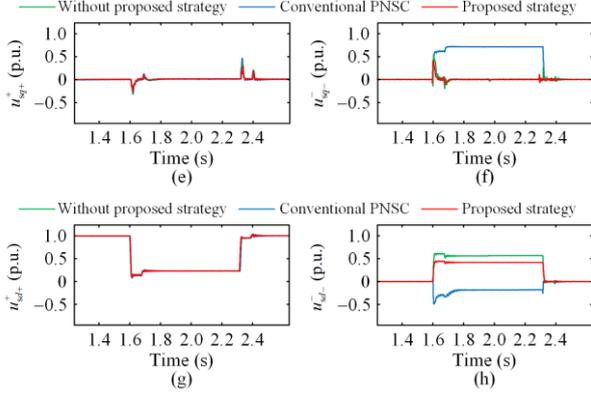


Fig. 19. comparison of simulation results under a two-phase-to-ground fault with different control strategies. (a) DCV of the DFIG system. (b) Output frequency of the PNS PLL (c) PS equivalent power angles. (d) NS equivalent power angles. (e) PS stator voltage  $q +$  axis components. (f) NS stator voltage  $q -$  axis components. (g) PS stator voltage  $d +$  axis components. (h) NS stator voltage  $d -$  axis components.

The simulation results of the DCV transient behaviors in the DFIG system under single-line-to-ground fault and phase-to-phase-fault are displayed in Figs. 20 and 21, respectively. The RSC and GSC current references are configured according to Cases 5 and 6 in Table AII. From Figs. 20 and 21, for both asymmetrical faults, the proposed strategy effectively eliminates  $\Delta W_s$ . Consequently, the DCV is restored to a new stable condition. Therefore, the proposed control strategy effectively improves both the TS level of the DCV and the transient performance of the DFIG system under different types of asymmetrical grid faults.

Stage 1: conventional positive and negative sequence reactive support strategy  
Stage 2: proposed positive and negative sequence active and reactive support strategy

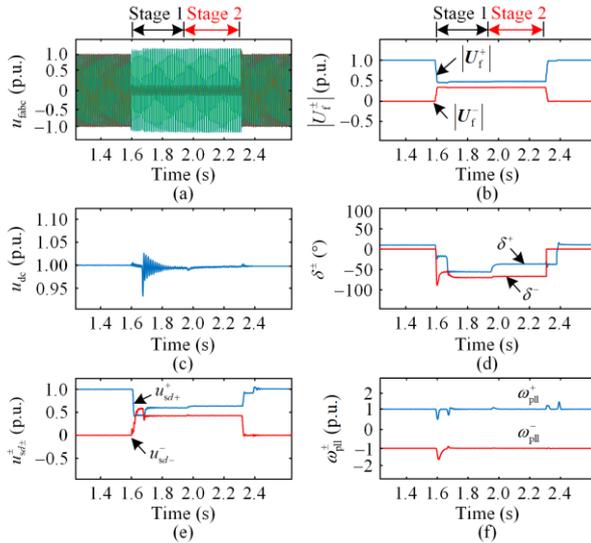


Fig. 20. Case 5: simulation results of the proposed strategy under a single-line-to-ground fault. (a) Three-phase voltage of the fault point. (b) Amplitude of the PNS fault voltage. (c) DCV of the DFIG system. (d) PNS equivalent power angles. (e) PNS  $d$ -axis voltage components. (f) Output frequency of the PNS PLL.

Stage 1: conventional positive and negative sequence reactive support strategy  
Stage 2: proposed positive and negative sequence active and reactive support strategy

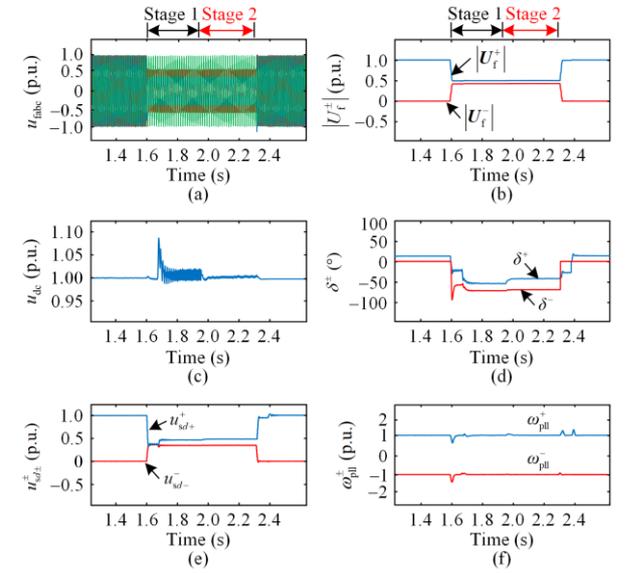


Fig. 21. Case 6: simulation results of the proposed strategy under a phase-to-phase fault. (a) Three-phase voltage of the fault point. (b) Amplitude of the PNS fault voltage. (c) DCV of the DFIG system. (d) PNS equivalent power angles. (e) PNS  $d$ -axis voltage components. (f) Output frequency of the PNS PLL.

Figure 22 presents the simulation results of the system with the proposed strategy under a three-phase fault. It can be observed that by adjusting the RSC and GSC active and reactive current references in accordance with the control characteristics of the DCV, the DCV can also be effectively regulated to  $U_{dc0}$ . Therefore, the control strategy proposed in this work can also be extended to symmetrical grid faults with proper modifications.

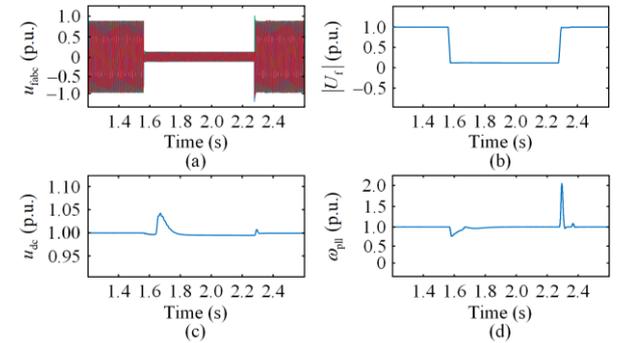


Fig. 22. Simulation results of the proposed strategy under a three-phase fault. (a) Three-phase voltage of the fault point. (b) Amplitude of the fault voltage. (c) DCV of the DFIG system. (d) Output frequency of the PLL.

## B. Experimental Validation

Experiments are conducted on a PLL-based DFIG system to further confirm the validity of the theoretical analysis and the proposed strategy. The experimental schematic diagram of the DFIG system is shown in Fig. 23, and Table AIII in Appendix A lists the detailed parameters of the experimental setup. A DC

motor is employed to drive the DFIG system, while the transmission line is simulated using reactors. Data collection is performed using an oscilloscope. Due to the

limitation of the experimental setup, only tests under phase-to-phase fault are carried out, implemented using a voltage divider in the AC grid.

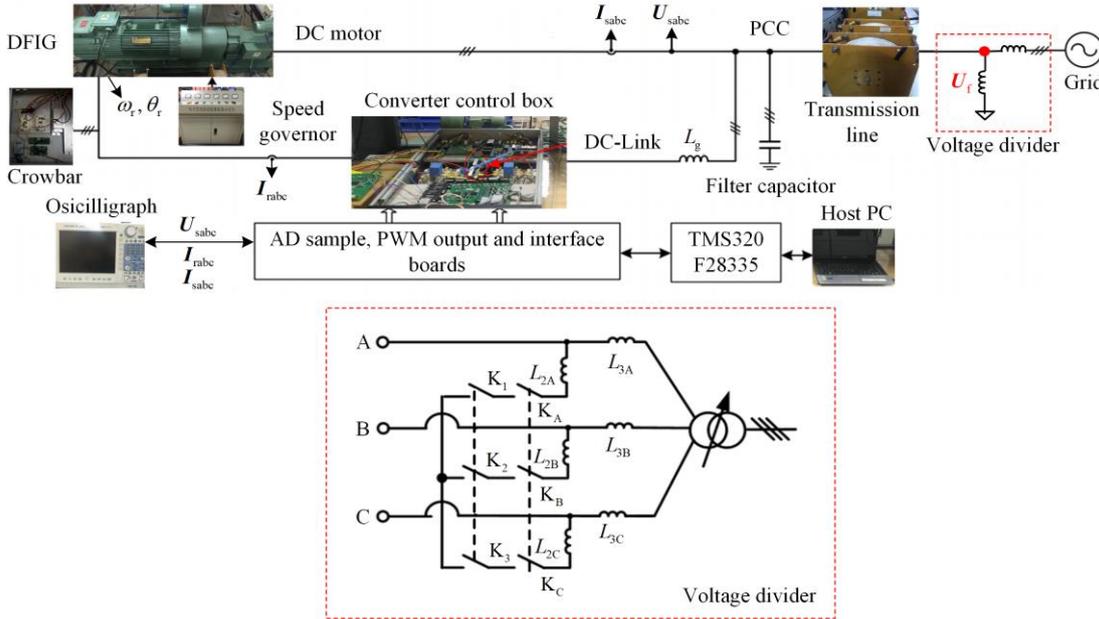


Fig. 23. Experimental detail diagram of the system.

Figure 24 shows the experimental instability results of the DFIG system. The grid fault occurs at 1.5 s, causing  $U_{fab}$  to decrease to 0.5 p.u. The RSC is instantly blocked at 1.6 s and adopts the single current loop control strategy. During the fault, the PNS current references of the RSC are set to  $-0.8$  p.u.,  $0.2$  p.u.,  $0$ , and  $0.3$  p.u., while the PNS current references of GSC are set to  $0.12$  p.u.,  $0$ , and  $0.1$  p.u., respectively. In this case,  $u_{sq+}^+$  and  $u_{sq-}^-$  cannot be controlled to 0, and according to (19), the disturbance of  $P_{do+}$  and  $P_{do-}$  cannot be eliminated. As shown in Fig. 24(b), the DC-link capacitor releases kinetic energy to balance  $\Delta E$ , leading to transient instability of the DCV. In addition, the output active and reactive power of the DFIG stator and rotor also become unstable, as shown in Figs. 24(d) and (e). These experimental results are consistent with the theoretical analysis.

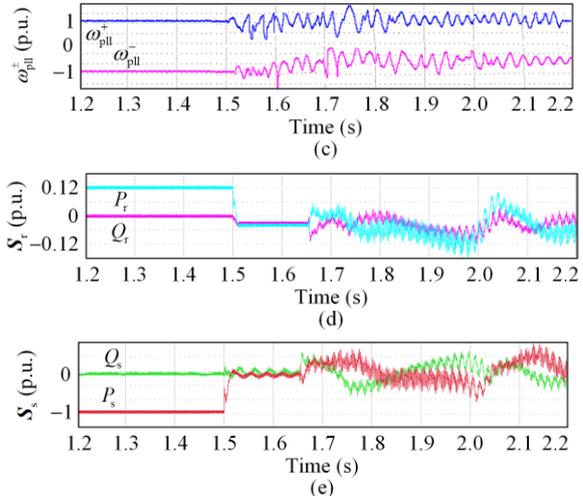
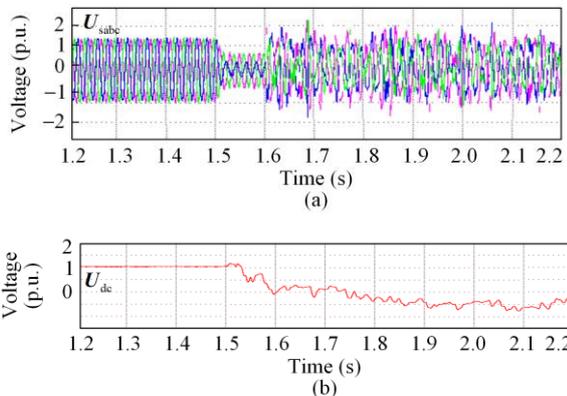


Fig. 24. Instability experimental results of the DFIG system. (a) Three-phase voltage of the DFIG stator. (b) The DC-link capacitor voltage. (c) Output frequency of the PNS PLL. (d) The output active and reactive power of the DFIG rotor. (e) The output active and reactive power of the DFIG stator.



Figures 25(a) and (b) illustrate the experimental results of the DFIG system under a phase-to-phase fault, with and without the application of the proposed control strategy, respectively. In Fig. 25, when  $U_{fab}$  decreases to 0.64 p.u. and the PNS active and reactive currents are solely provided by the RSC, as shown in Fig. 25 (a), the DCV diverges from  $U_{dc0}$ . By contrast, when the proposed strategy is applied, the disturbance of  $P_{do}$  and the imbalance between  $P_D$  and  $P_{so}$  are completely eliminated. As a result, the DCV is promptly restored to a

new stable state. Moreover, the positive and negative PLLs are also quickly restored to synchronous operation under the proposed method, which indicates that the system has a stronger capability to maintain transient synchronization with the grid during faults. Therefore, the transient response of the DCV and the TS of the DFIG system are effectively improved by the proposed control strategy.

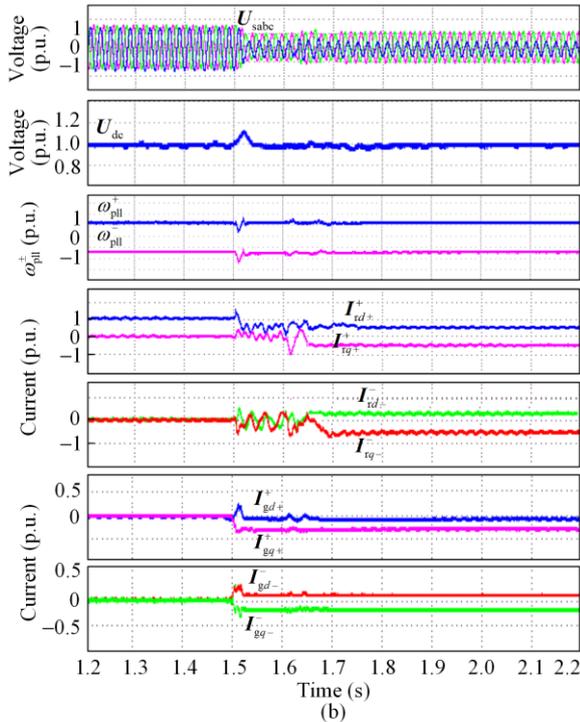
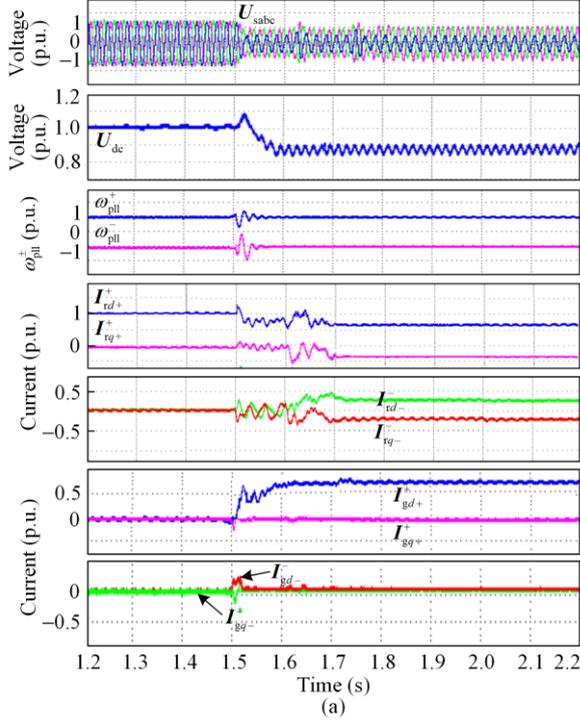


Fig. 25. Experimental comparison of the DFIG system. (a) Results without the proposed method. (b) Results with the proposed method.

## VI. CONCLUSION

This paper thoroughly investigates the transient behavior and stability control strategy of DCV in PLL-based DFIG systems during asymmetrical grid faults. The findings can effectively prevent wind farms from disconnecting from the grid and avoid large-scale economic losses. The conclusions are summarized as follows.

1) Based on the dual-sequence synchronization nonlinear transient model, the equivalent damping power, the equivalent static stable power, and the equivalent dynamic stable power are the key factors that affect the transient behavior of the DCV. These components are influenced by the control characteristics of the PNS PLL and by the power coordination between the RSC and GSC.

2) The impact of different parameters on the DCV TS is analyzed, and the TS process of the DCV under asymmetrical grid faults is described. The findings show that if dynamic stable power cannot be eliminated, the DCV will exhibit different instability phenomena. Furthermore, when the imbalance between the static stable power and damping power cannot be mitigated, the DCV will display different levels of TS during asymmetrical faults. Therefore, the influence of the transient behavior among converters on the TS of smart grids with high renewable energy penetration cannot be overlooked, and it should be considered when designing LVRT control strategies.

3) A control strategy is introduced to enhance the TS of the DCV during asymmetrical grid faults. Through the coordination of the LVRT current references of the RSC and GSC, the strategy effectively improves the TS level of the DCV under such faults. It should be noted that the effectiveness of this approach relies on the rapid acquisition of fault information, and further investigation is needed on how to obtain this information promptly in a real-world grid-connected DFIG systems.

## APPENDIX A

### A. Simulation Parameters

TABLE AI  
PARAMETERS OF THE SIMULATION DFIG SYSTEM

Symbol	Description	Value
$S_N$ (MW)	Rated power	200
$U_N$ (V/kV)	Rated voltage	690/35/220
$U_{dc}$ (V)	DC-link voltage	1200
$R_s, L_s$ (p.u.)	Stator resistance and inductance	0.0189, 2.68
$R_r, L_r$ (p.u.)	Rotor resistance and inductance	0.016, 2.54
$Z_f$ (p.u.)	Fault impedance	0.009
$p$	Pole pair	2
$C$ ( $\mu$ F)	DC-link capacitor	18
$L_m$ (mH)	Mutual inductance	16.5
$Z_L^+ = Z_L^-$ (p.u.)	Line impedance	$0.059 + j0.963$
$Z_L^0$ (p.u.)	Line zero sequence impedance	$0.123 + j0.899$
$Z_{L-f}$ (p.u.)	Phase-phase impedance	0.0397

## B. Current References

TABLE AII  
CURRENT REFERENCES OF SIMULATION CASES

Case	(p.u.)						
	$i_{rd+}^{+*}$	$i_{rq+}^{+*}$	$i_{rd-}^{-*}$	$i_{rq-}^{-*}$	$i_{gd+}^{+*}$	$i_{gd-}^{-*}$	$i_{gq-}^{-*}$
1	0.95	-0.2	0	-0.15	0.2	0	0
2	0.35	-0.31	0.15	-0.35	0.12	-0.1	0.11
3	0.23	-0.38	0.11	-0.42	0.22	-0.07	0.21
4	0.12	-0.45	0.05	-0.43	0.28	-0.05	0.25
5	0.25	-0.36	0.12	-0.39	0.21	-0.09	0.18
6	0.39	-0.14	0.18	-0.27	0.13	-0.13	0.15

## C. Experimental Parameters

TABLE AIII  
PARAMETERS OF THE EXPERIMENTAL DFIG SYSTEM

Symbol	Description	Value
$S_N$ (kW)	Rated power	1.5 (1 p.u.)
$U_N$ (V)	Rated voltage	220 (1 p.u.)
$U_{dc}$ (V)	DC-link voltage	400 (1 p.u.)
$f_n$ (Hz)	Frequency	50 (1 p.u.)
$L_s$ (mH), $R_s$ ( $\Omega$ )	Stator inductance and resistance	66.6, 1.09
$L_m$ (mH)	Mutual inductance	64.6
$L_r$ (mH), $R_r$ ( $\Omega$ )	Rotor inductance and resistance	66.6, 1.5
$L$ (mH), $R$ ( $\Omega$ )	Transmission line inductance and resistance	21, 1.5
$C$ ( $\mu$ F)	DC-link capacitor	30
$p$	Pole pair	2
$k_{p\_pll\_fault}^+$ , $k_{i\_pll\_fault}^+$	PI gains of the positive PLL	0.04, 0.005
$k_{p\_pll\_fault}^-$ , $k_{i\_pll\_fault}^-$	PI gains of the negative PLL	0.065, 0.015

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Not applicable.

### AUTHORS' CONTRIBUTIONS

Yi Luo: modeling the system, designing the algorithm of control strategy, and writing the paper. Jun Yao: putting forward the initial concept and giving technical guidance in the whole process. Dong Yang: checking the data and experimental results. Hai Xie: demonstrating the strategy proposed in the paper. Linsheng Zhao: giving technical guidance in the whole process. Rongyu Jin: typesetting the manuscript. All authors read and approved the final manuscript.

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### AVAILABILITY OF DATA AND MATERIALS

Not applicable.

### DECLARATIONS

Competing interests: The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this article.

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