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Transient synchronous stability analysis and enhancement control strategy of a PLL-based VSC system during asymmetric grid faults

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Abstract

The stability of a voltage source converters (VSC) system based on phase-locked loop (PLL) is very important issue during asymmetric grid faults. This paper establishes a transient synchronous stability model of a dual-sequence PLL-based VSC system during low voltage ride-through by referring to the equivalent rotor swing equation of synchronous generators. Based on the model, the synchronization characteristics of the VSC system under asymmetric grid faults are described, and the interaction mechanisms, as well as the transient instability phenomena of positive and negative sequence PLL during asymmetric faults are explained. Using the equal area criterion, the influences of sequence control switching action, detection delay, and interaction between the positive and negative sequence PLL on the transient synchronous stability of the VSC system are analyzed, respectively. In addition, a transient stability assessment criterion based on the critical fault clearance angle and time and an enhancement control strategy based on the improved positive and negative sequence PLL are proposed. Finally, the analytical results are validated through simulation and experiments.

Keywords Voltage source converters (VSC), Asymmetric grid faults, Dual-sequence PLL, Transient synchronous stability, Assessment criterion, Enhancement control strategy

1 Introduction

With the depletion of traditional fossil energy, renewable power generation (RPG) technology represented by wind power and photovoltaics has developed rapidly [1]. However, owing to the distribution of renewable energy resources, a large number of renewable energy plants are located in remote areas and are connected to the grids via

long transmission lines, which are prone to short-circuit faults, especially asymmetric grid faults [2–5]. In addition, the latest grid codes in many countries have raised the requirements of reactive power support for RPG systems during the period of asymmetric grid faults [6, 7]. Consequently, studies on the transient stability of RPG during asymmetric grid faults are crucial.

Recently, loss of synchronization (LOS) events between RPG systems and grids have been reported many times [8], and have attracted widespread concern. For the transient stability of an RPG system under grid faults, existing researches mainly focused on two aspects. One is the existence of an equilibrium point, and the other is the ability of the system to reach a new equilibrium point after faults [9]. Voltage source converters (VSC) are widely used in the grid-connected interface of an RPG

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system, such as wind power and photovoltaics, and reference [10] investigates the transient stability of the VSC system during symmetric grid faults, and indicates that LOS is inevitable in the absence of an equilibrium point when a grid fault occurs. In addition, references [11, 12] find that the phase of the terminal voltage is a key factor that influences the existence of an equilibrium point for an RPG system, and there is the coupling phenomenon between the terminal voltage and frequency of the system. However, even if the equilibrium point exists [13, 14], an improper initial state or poor dynamic characteristics of the system can lead to LOS. References [15–18] point out that intense interaction behaviors between PLL and the grid can lead to transient instability for the system. By using a Lyapunov function [19] and the equal area criterion (EAC), references [14, 20] establish the large signal models of a VSC system, which is similar to the rotor swing equation of an SG, and explain the physical mechanism of transient instability of the system at different fault stages. However, these studies only explore the transient stability of the VSC system during a symmetric fault, while most of short circuit faults in power systems are asymmetric [21].

Compared with the transient operational characteristics under symmetric grid faults, those of an RPG system under asymmetric grid faults are more complex. Reference [22] indicates that during asymmetric grid faults, RPG system not only has positive, negative and zero sequence components simultaneously, but also has a coupling relationship between the positive and negative sequence components. Moreover, reference [23] further points out that both the positive and negative sequence components of the system have the ability to keep synchronization with the grid during asymmetric grid faults, which need to be controlled individually and precisely. Therefore, in [24–26], the decoupled double synchronous reference frame PLL (DDSRF-PLL) models are established to accurately control the transient components of the system, and study the influence of the positive and negative sequence reactive currents injection on the transient stability of the system during asymmetric grid faults. However, in the above studies, the negative angular frequency is considered to be the inverse of the positive angular frequency. Reference [26] points out that the output angle of the negative sequence PLL is significantly different from that of positive sequence PLL during asymmetric grid faults, and so needs to be studied separately. In addition, it is a prerequisite for analyzing the transient stability of a VSC system to accurately depict the operating characteristics of the system under grid faults [27, 28]. However, the above studies rarely consider the influence of fault detection delay, nonlinear control behaviors [29] and other factors on the transient

stability of a VSC system under unbalanced fault conditions. Therefore, it is difficult to accurately depict the transient synchronous stability process of the VSC system during asymmetric grid faults.

To fill the gaps, this paper aims to reveal the influence of sequence control switching action, fault detection delay and interaction between the positive and negative sequence components on the transient synchronous stability of the PLL-based grid connected VSC system during asymmetric grid faults. Based on previous analyses, a corresponding transient stability criterion and enhancement control strategy are proposed. The main contributions of this paper are as follows:

1. A novel large-signal model and analysis method for the dual-sequence transient synchronization stability of the VSC system are presented, one which considers the coupled characteristics between the positive and negative sequence components and the nonlinear control characteristics of PLL.
2. Considering the influence mechanism of sequence control switching action, detection delay and interaction between the positive and negative sequence PLL on the transient behaviors and instability phenomenon of a VSC system, a novel transient synchronous stability evaluation criterion for the VSC system during asymmetric grid faults is proposed.
3. By increasing the critical fault clearance time and reducing detection delay time, an enhancement control strategy for a VSC system under asymmetric grid faults is proposed, one which is based on the improved positive and negative sequence PLL.

The rest of the paper is structured as follows: Sect. 2 depicts the transient characteristics of a VSC system during asymmetric faults, while Sect. 3 analyzes the transient synchronous mechanism, instability criterion and enhancement control strategy of the system during asymmetric grid faults. In Sect. 4, the simulation and experimental results are presented, and Sect. 5 concludes the paper.

2 Transient characteristics of the VSC systems during asymmetric faults

Figure 1 shows the control block diagram of the PLL-based VSC system under asymmetric grid faults, in which the transformer T_1 adopts the Y/ Δ connection type. During the pre-fault period, the external power control loop and internal current control loop structures are used to realize maximum power point tracking (MPPT) control (Flag=1). When the grid voltage (U_g) dip is detected, the VSC system only adopts the

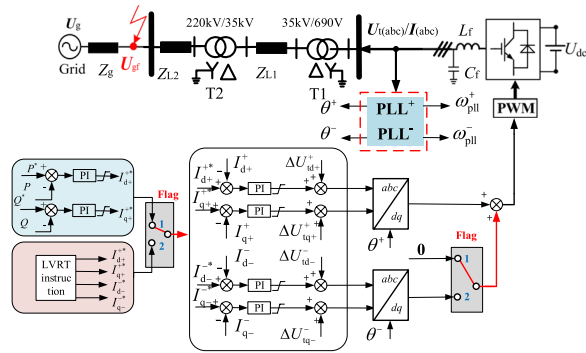


Fig. 1 The control block diagram of a VSC system under asymmetric grid faults

single current loop control (Flag=2), to realize the rapid injection of positive and negative sequence reactive currents during LVRT.

Since the analysis methods adopted in this paper are applicable to all types of asymmetric grid faults, the transient stability of the VSC system during asymmetric faults is analyzed in detail by taking the single-line-to-ground (SLG) fault as an example. When an SLG fault occurs, the VSC system has all of positive, negative and zero sequence components [21]. However, since both transformers T_1 and T_2 adopt the Y/ Δ connection type, there is no zero-sequence path in the system. Therefore, this paper only considers the influence of the positive and negative sequence components on the terminal voltage (U_t). This can be expressed as:

$$U_t = U_t^+ + U_t^- \quad (1)$$

Since the output angle of the negative sequence PLL is significantly different from that of the positive sequence PLL during asymmetric grid faults [23], positive and negative sequence PLL should be established to accurately track the positive and negative sequence grid voltages, respectively. Here, a $dq_{\pm}$ reference frame is established, as shown in Fig. 2.

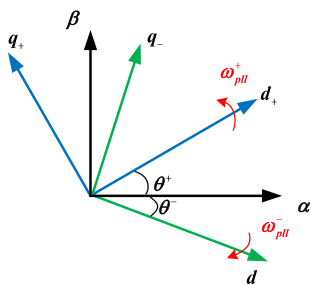


Fig. 2 Spatial position diagram of PLL $dq_{\pm}$ reference frames

Considering the coupled characteristics between the positive and negative sequence components, the composite sequence network diagram [23] of the VSC system when an SLG fault occurs is shown in Fig. 3. As shown, considering that the RPG source with the VSC as the interface is not directly connected to the grid, but through large DC-link capacitance, this makes the sources have excellent anti-disturbance ability for grid voltage. Meanwhile, as the grid faults occur within a short time from tens of milliseconds to hundreds of milliseconds, control schemes [30] can be adopted by the converter between the generating source and the DC-link capacitance to maintain the DC-link voltage as stable during the faults. Consequently, the DC-link voltage can be assumed to be stable during grid faults [9, 10], and the characteristics of the generating sources will not affect the transient stability of the VSC system. Therefore, the positive and negative sequence currents provided by the VSC system are regarded as the controlled current sources I^+ and I^- with frequencies of ω_{pll}^+ and ω_{pll}^- , respectively. Z_t^+ and Z_t^- are regarded as the positive and negative sequence equivalent impedances of the filter circuit, respectively. In addition, the positive sequence current source provides over-excited reactive power, while the negative sequence current source provides under-excited reactive power [23]. The currents injected by the system can be expressed as:

$$\begin{cases} I = I^+ + I^- \\ I^+ = I^+ \angle (\theta^+ + \theta_0^+) \\ I^- = I^- \angle (\theta^- + \theta_0^-) \end{cases} \quad (2)$$

where θ_0^+ and θ_0^- are the angles of I^+ in the dq_+ reference frame and I^- in the dq_- reference frame, respectively. θ^+ is the angle between the α and d_+ axes, while θ^- is the angle between the α and d_- axes. In addition, the superscript represents the positive and negative sequence components, while the subscript represents the $dq_{\pm}$ reference frame.

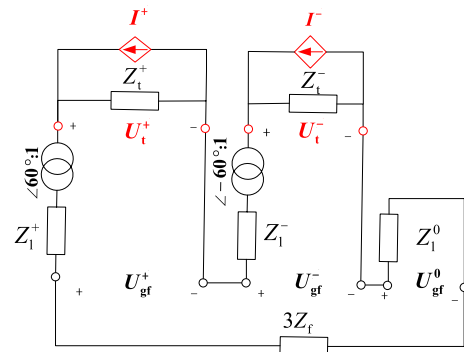


Fig. 3 Sequence domain circuit under SLG fault

From Fig. 3, $\mathbf{U}_t$ can be shown as:

$$\begin{cases} \mathbf{U}_t^+ = K_1 \mathbf{U}_{gf}^+ e^{-j\frac{\pi}{3}} - K_2 \mathbf{U}_{gf}^- e^{-j\frac{\pi}{3}} - K_3 \mathbf{U}_{gf}^0 e^{-j\frac{\pi}{3}} + \mathbf{I}^+ Z_4 - \mathbf{I}^- Z_5 e^{-j\frac{2\pi}{3}} \\ \mathbf{U}_t^- = -K_6 \mathbf{U}_{gf}^+ e^{j\frac{\pi}{3}} + K_7 \mathbf{U}_{gf}^- e^{j\frac{\pi}{3}} - K_8 \mathbf{U}_{gf}^0 e^{j\frac{\pi}{3}} + \mathbf{I}^+ Z_9 - \mathbf{I}^- Z_{10} e^{j\frac{2\pi}{3}} \end{cases} \quad (3)$$

where $K_i = |K_i| \angle \phi_i$, $i=1, 2, 3, 6, 7$, and 8 , and $Z_i = |Z_i| \angle \phi_i$, $i=4, 5, 9$ and 10 , as summarized in the Appendix.

When the terminal voltage is excited by the positive or negative sequence current of the system, it will have the same frequency as the excitation current and its conjugate quantity [22]. In addition, since $\mathbf{U}_{gf}^0$ cannot stimulate the corresponding fault current at the terminal of the system, its effect can be ignored. Therefore, transforming (3) into the $dq_{\pm}$ reference frame yields:

$$\begin{cases} u_{td+} + ju_{tq+} = |K_1| \mathbf{U}_{gf}^+ \angle(\varphi_1 - \delta^+) - K_2 \mathbf{U}_{gf}^- \angle(\varphi_2 - \delta^+ + \delta^-) + \mathbf{I}^+ |Z_4| \angle(\varphi_4 + \theta_0^+) \\ \quad - \mathbf{I}^- |Z_5| \angle(\varphi_5 + \theta_0^- - \delta^+ + \delta^-) - \text{conj}(\mathbf{I}^- |Z_5| \angle(\varphi_5 + \theta^-)) \\ u_{td-} + ju_{tq-} = -|K_6| \mathbf{U}_{gf}^+ \angle(\varphi_6 + \delta^+ - \delta^-) + K_7 \mathbf{U}_{gf}^- \angle(\varphi_7 - \delta^-) + |Z_9| \mathbf{I}^+ \angle(\varphi_9 + \theta_0^-) \\ \quad - |Z_{10}| \mathbf{I}^+ \angle(\varphi_{10} + \theta_0^+ + \delta^+ - \delta^-) - \text{conj}(|Z_{10}| \mathbf{I}^+ \angle(\varphi_{10} + \theta^+)) \end{cases} \quad (4)$$

where δ^+ is the angle difference between the positive sequence terminal voltage ($\mathbf{U}_t^+$) and grid voltage ($\mathbf{U}_{gf}^+$), and δ^- is the angle difference between $\mathbf{U}_t^-$ and $\mathbf{U}_{gf}^-$, $\text{conj}(\mathbf{I}^- |Z_5| \angle(\varphi_5 - \theta^-))$ is the coupled voltage component generated by the negative sequence current, while $\text{conj}(\mathbf{I}^+ |Z_{10}| \angle(\varphi_{10} - \theta^+))$ is the coupled voltage component generated by the positive sequence current.

To eliminate the double frequency components in the $dq_{\pm}$ reference frame, the moving average filter (MAF) is widely used to extract the positive and negative sequence components [31]. Therefore, a dual-sequence PLL model based on MAF is established in this paper, as shown in Fig. 4.

According to the PLL control structure, the dynamic model of the positive and negative sequence PLL can be obtained as:

$$\begin{cases} \omega_{pll}^+ = k_p U_{tq+}^+ + k_i \int U_{tq+}^+ dt + \omega_g \\ \frac{d\omega_{pll}^+}{dt} = k_p \frac{dU_{tq+}^+}{dt} + k_i U_{tq+}^+ \end{cases} \quad (5)$$

$$\begin{cases} \omega_{pll}^- = k_p U_{tq-}^- + k_i \int U_{tq-}^- dt - \omega_g \\ \frac{d\omega_{pll}^-}{dt} = k_p \frac{dU_{tq-}^-}{dt} + k_i U_{tq-}^- \end{cases} \quad (6)$$

The change rate of u_{tq+}^+ and u_{tq-}^- can be expressed as:

$$\begin{cases} \frac{du_{tq+}^+}{dt} = -U_{gf}^+ |K_1| \cos(\delta^+ - \varphi_1) \frac{d\delta^+}{dt} + L_{eq4} I_{d+}^+ \frac{d\omega_{pll}^+}{dt} - L_{eq5} I_{d+}^+ \frac{d\omega_{pll}^-}{dt} \\ \frac{du_{tq-}^-}{dt} = -U_{gf}^- |K_7| \cos(\delta^- - \varphi_7) \frac{d\delta^-}{dt} + L_{eq9} I_{d-}^- \frac{d\omega_{pll}^-}{dt} - L_{eq10} I_{d-}^- \frac{d\omega_{pll}^+}{dt} \end{cases} \quad (7)$$

where $L_{eq4} = (Z_4 \sin \phi_4) / (2\pi \omega_{pll}^+)$, $L_{eq5} = (Z_5 \sin \phi_5) / (2\pi \omega_{pll}^-)$, $L_{eq9} = (Z_9 \sin \phi_9) / (2\pi \omega_{pll}^-)$, and $L_{eq10} = (Z_{10} \sin \phi_{10}) / (2\pi \omega_{pll}^+)$.

Substituting (5) and (6) into (7), yields:

$$\begin{cases} \frac{d\omega_{pll}^+}{dt} = k_p (-U_{gf}^+ |K_1| \cos(\delta^+ - \varphi_1) \frac{d\delta^+}{dt} + L_{eq4} I_{d+}^+ \frac{d\omega_{pll}^+}{dt} - L_{eq5} I_{d+}^+ \frac{d\omega_{pll}^-}{dt} \\ \quad + k_i (-|K_1| U_{gf}^+ \sin(\delta^+ - \varphi_1) + R_{eq4} I_{q+}^+ + \omega_{pll}^+ L_{eq4} I_{d+}^+ - R_{eq5} I_{q+}^- - \omega_{pll}^- L_{eq5} I_{d+}^+) \\ \frac{d\omega_{pll}^-}{dt} = k_p (-U_{gf}^- |K_7| \cos(\delta^- - \varphi_7) \frac{d\delta^-}{dt} + L_{eq9} I_{d-}^- \frac{d\omega_{pll}^-}{dt} - L_{eq10} I_{d-}^- \frac{d\omega_{pll}^+}{dt}) \\ \quad + k_i (-|K_7| U_{gf}^- \sin(\delta^- - \varphi_7) + R_{eq9} I_{q-}^- + \omega_{pll}^- L_{eq9} I_{d-}^- - R_{eq10} I_{q-}^+ - \omega_{pll}^+ L_{eq10} I_{d-}^-) \end{cases} \quad (8)$$

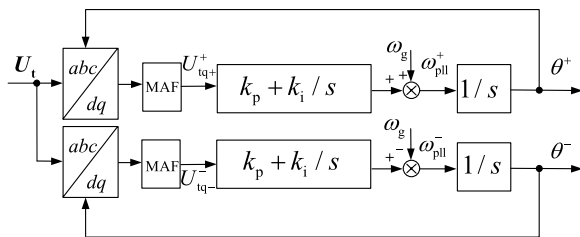


Fig. 4 PLL control structure based on positive and negative sequence separation

From (5) and (6), δ^+ and δ^- can be expressed as:

$$\begin{cases} \frac{d\delta^+}{dt} = \omega_b (\omega_{pll}^+ - \omega_g) \\ \frac{d\delta^-}{dt} = \omega_b (\omega_{pll}^- + \omega_g) \end{cases} \quad (9)$$

By referring to the SG, the positive and negative sequence transient synchronous characteristics of the VSC system can be obtained as (10), where $R_{eq4} = Z_4 \cos \phi_4$, $R_{eq5} = Z_5 \cos \phi_5$, $R_{eq9} = Z_9 \cos \phi_9$, $R_{eq10} = Z_{10} \cos \phi_{10}$.

In (10), J_{v+}^+ and D_{v+}^+ are the positive sequence virtual inertia and damping coefficients, respectively, whereas, J_{v+}^- and D_{v+}^- are the negative sequence coupled virtual inertia and damping coefficient, respectively. These reflect the influence of the negative sequence components on the transient stability of the positive sequence PLL. In addition, J_{v-}^- and D_{v-}^- are the negative sequence virtual inertia and damping coefficients, respectively, while J_{v-}^+ and D_{v-}^+ are the positive sequence coupled virtual inertia and damping coefficient, respectively. These reflect the influence of the positive sequence components on the transient stability of negative sequence PLL. Furthermore, T_{v+}^* and T_{v+}^+ are the positive sequence virtual prime mover torque and electromagnetic torque, respectively. Similarly, T_{v-}^* and T_{v-}^- are the negative sequence virtual prime mover and electromagnetic torque, respectively.

In addition, it can be seen in (10) that T_{v+}^* consists of the positive sequence prime mover torque ($R_{eq4}I_{q+}^+ + \omega_g L_{eq4}I_{d+}^+$) and the negative sequence coupled prime mover torque ($R_{eq5}I_{q+}^- + \omega_g L_{eq5}I_{d+}^-$). This reflects the interaction between the positive sequence output power and grid. Similarly, T_{v-}^* consists of the negative sequence prime mover torque ($R_{eq9}I_{q-}^- - \omega_g L_{eq9}I_{d-}^-$) and the positive sequence coupled prime mover torque ($R_{eq10}I_{q-}^+ + \omega_g L_{eq10}I_{d-}^+$), and this reflects the interaction between the negative sequence output power and grid. T_{v+}^+ and T_{v-}^- reflect the clamping effect of the positive and negative sequence grid voltage on the transient stability of the system, respectively.

From (10), the positive and negative sequence nonlinear dynamic control model can be obtained, as shown in Fig. 5. Although the positive and negative sequence PLL models are established separately in this paper, under the

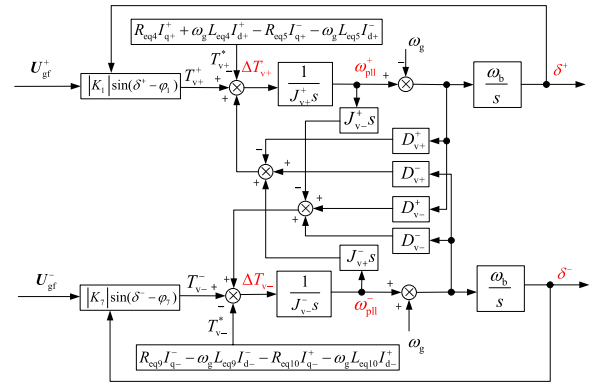


Fig. 5 Equivalent dynamic control model of the VSC system

interaction between the PLL and grid, the dynamic behavior of positive and negative sequence PLL will influence each other during the faults. As shown in (10) and Fig. 5, under the actions of the coupled prime mover ($R_{eq10}I_{q-}^+ + \omega_g L_{eq10}I_{d-}^+$) or ($R_{eq5}I_{q+}^- + \omega_g L_{eq5}I_{d+}^-$), when the working state of the positive or negative sequence PLL changes, the unbalanced virtual torque ΔT_{v+} and ΔT_{v-} will occur simultaneously. In these conditions, ΔT_{v+} or ΔT_{v-} will lead both ω_{pll}^+ and ω_{pll}^- to change. δ^- and δ^+ also will also tend to change. Then, through the negative feedback branches, δ^- and δ^+ will adjust T_{v+}^+ and T_{v-}^- to eliminate $\Delta T_{v\pm}$. Finally, if ΔT_{v+} and ΔT_{v-} are both eliminated, ω_{pll}^+ and ω_{pll}^- will be restored to the corresponding ω_g and $-\omega_g$ under the actions of the damping coefficients (D_{v+}^+ , D_{v+}^- , D_{v-}^+ , and D_{v-}^-). In addition, due to the existence of coupled virtual damping coefficients (D_{v+}^- , D_{v-}^+), the positive or negative sequence PLL will provide damping effect on the transient synchronization of the other PLL.

$$\begin{cases}
 \left(\frac{1 - k_p L_{eq4} I_{d+}^+}{k_i} \right) \frac{d\omega_{pll}^+}{dt} + \frac{k_p L_{eq5} I_{d+}^-}{k_i} \frac{d\omega_{pll}^-}{dt} = \underbrace{R_{eq4} I_{q+}^+ + \omega_g L_{eq4} I_{d+}^+ - R_{eq5} I_{q+}^- - \omega_g L_{eq5} I_{d+}^-}_{T_{v+}^*} - \underbrace{U_{gf}^+ |K_1| \sin(\delta^+ - \varphi_1)}_{T_{v+}^+} \\
 + \underbrace{\left(\frac{k_p}{k_i} L_{eq5} I_{d+}^- \right) (\omega_{pll}^- + \omega_g)}_{D_{v+}^-} - \underbrace{\left(\frac{k_p}{k_i} (\omega_b U_{gf}^+ |K_1| \cos(\varphi_1 - \delta^+) - L_{eq4} I_{d+}^+) \right) (\omega_{pll}^+ - \omega_g)}_{D_{v+}^+} \\
 \left(\frac{1 - k_p L_{eq9} I_{d-}^-}{k_i} \right) \frac{d\omega_{pll}^-}{dt} + \frac{k_p L_{eq10} I_{d-}^+}{k_i} \frac{d\omega_{pll}^+}{dt} = \underbrace{R_{eq9} I_{q-}^- - \omega_g L_{eq9} I_{d-}^- - R_{eq10} I_{q-}^+ - \omega_g L_{eq10} I_{d-}^+}_{T_{v-}^*} - \underbrace{U_{gf}^- |K_7| \sin(\delta^- - \varphi_7)}_{T_{v-}^-} \\
 + \underbrace{\left(\frac{k_p}{k_i} (\omega_b U_{gf}^- |K_7| \cos(\varphi_7 - \delta^-) - L_{eq9} I_{d-}^-) \right) (\omega_{pll}^- + \omega_g)}_{D_{v-}^-} - \underbrace{\left(\frac{k_p}{k_i} L_{eq10} I_{d-}^+ \right) (\omega_{pll}^+ - \omega_g)}_{D_{v-}^+}
 \end{cases} \quad (10)$$

The dynamic behavior of the positive and negative sequence PLL during the fault means that they will influence each other. Thus, only if no LOS phenomenon occurs in either the positive or negative sequence PLL, the VSC system may maintain transient synchronous stability with the grid.

3 Analysis of the transient synchronous mechanism during asymmetric faults

According to the analysis presented in Sect. 2, the coupled relationship between the positive and negative sequence PLL, as well as the operating conditions of the system, can seriously affect the transient stability of the VSC system. Therefore, this section will further explore the influence mechanism of the interaction between the positive and negative sequence PLL, as well as the control switching action, on the existence of the equilibrium point and the transient synchronous process of the system. Considering that the mainstream grid fault detection methods have at least 5–20 ms of detection delay [32], therefore, this paper will also further explore the influence of fault detection delay on the transient characteristics of the VSC system during asymmetric grid faults.

3.1 Synchronous characteristics at fault detection stage

As assumed in Sect. 2, the VSC system works in MPPT and the unity power factor state during the pre-fault stage. When an SLG fault occurs, the current injected by the system during the fault detection stage is given as:

$$I_{d+}^{+*} = \begin{cases} \frac{P_n^*}{U_{tdf}}, & \left(\frac{P_n^*}{U_{tdf}} < I_{dm} \right) \\ I_{dm}, & \left(\frac{P_n^*}{U_{tdf}} \geq I_{dm} \right) \end{cases} \quad (11)$$

where I_{dm} is the current limiting value of the grid side converter (GSC).

Considering that although VSC is still working in Flag 1 mode at the fault detection stage, because of the weak anti-disturbance of power electronic devices, the fault current of the system will increase rapidly to the current threshold (I_{dm}) under severe grid faults. In this case, the outer power control loop will saturate and lose its function. As a result, $I_{d+}^{+*} = I_{dm}$ at the fault detection stage.

Since there is no requirement of the negative sequence active current in the latest grid codes [6, 7], the VSC system does not provide negative sequence active current in this paper. Therefore, when the LVRT strategy is triggered, the current references of the system will be set directly, as:

$$\begin{cases} I_{d+}^{+*} = I_{d+}^{+*} & I_{q+}^{+*} = I_{q+}^{+*} \\ I_{d-}^{+*} = I_{d-}^{+*} = 0 & I_{q-}^{+*} = I_{q-}^{+*} \end{cases} \quad (12)$$

In addition, because of the detection delay of the system, the LVRT strategy of the system is not triggered immediately at the fault detection stage. Consequently, the system will still output current according to the instructions before the fault occurrence, which means that the system only provides active current at the fault detection stage.

In combination with (10)–(12), Eq. (13) can be obtained, which reflects the synchronous characteristics of the positive and negative sequence PLL at the fault detection stage.

$$\left\{ \begin{array}{l} \underbrace{\left(\frac{1 - k_p L_{eq4} I_{d+}^{+*}}{k_i} \right)}_{I_{v+}^{+*}} \frac{d\omega_{pll}^{+}}{dt} = \underbrace{\omega_g L_{eq4} I_{dm}}_{T_{v+}^{+*}} - \underbrace{U_{gf}^{+} |K_1| \sin(\delta^{+} - \varphi_1)}_{T_{v+}^{+*}} \\ - \underbrace{\left(\frac{k_p}{k_i} (\omega_b U_{gf}^{+} |K_1| \cos(\varphi_1 - \delta^{+}) - L_{eq4} I_{dm}) \right)}_{D_{v+}^{+*}} (\omega_{pll}^{+} - \omega_g) \\ \underbrace{\left(\frac{1}{k_i} \right)}_{I_{v-}^{+*}} \frac{d\omega_{pll}^{-}}{dt} + \underbrace{\frac{k_p L_{eq10} I_{dm}}{k_i}}_{I_{v-}^{+*}} \frac{d\omega_{pll}^{+}}{dt} = \underbrace{-\omega_g L_{eq10} I_{dm}}_{T_{v-}^{+*}} - \underbrace{U_{gf}^{-} |K_7| \sin(\delta^{-} - \varphi_7)}_{T_{v-}^{+*}} \\ + \underbrace{\left(\frac{k_p}{k_i} L_{eq10} I_{dm} \right)}_{D_{v-}^{+*}} (\omega_{pll}^{+} - \omega_g) - \underbrace{\left(\frac{k_p}{k_i} (\omega_b U_{gf}^{-} |K_7| \cos(\varphi_7 - \delta^{-})) \right)}_{D_{v-}^{+*}} (\omega_{pll}^{-} + \omega_g) \end{array} \right. \quad (13)$$

3.2 Influence on the existence of positive and negative sequence equilibrium points

According to (10), the existence of the positive and negative sequence coupled components influences the virtual prime mover torque $T_{v\pm}^*$, while the interaction between $T_{v\pm}^*$ and $T_{v\pm}^\pm$ directly affects the balance of torque. Therefore, the interaction between the positive and negative sequence PLL will also significantly affect the existence of the positive and negative sequence equilibrium points, as well as the instability forms of the system. By analyzing the interaction relationships between $T_{v\pm}^*$ and $T_{v\pm}^\pm$, there are four cases where the positive and negative sequence equilibrium points of the VSC system do not exist. These are shown in Fig. 6a–d, respectively.

As shown in Fig. 6a, if the minimum value of T_{v+}^+ is greater than T_{v+}^* , the unbalanced torque (ΔT_{v+}) will drive ω_{pll}^+ to decrease. From Fig. 5, $|\omega_{pll}^-|$ will also decrease under the action of the positive sequence coupled components. Finally, the LOS phenomena for angular frequency decrease will occur in both the positive and negative sequence PLL. In addition, in this case, according to (10), the negative sequence coupled components prevent T_{v+}^* from moving downward. This is conducive to the transient stability of the system.

When the maximum value of T_{v+}^+ is less than T_{v+}^* , as is shown in Fig. 6b, $(T_{v+}^* - T_{v+}^+) > 0$ will be always valid, and continue to drive ω_{pll}^+ and $|\omega_{pll}^-|$ to increase, eventually leading to the LOS phenomena for angular frequency increase in the positive and negative sequence PLL. In this case, the negative sequence coupled components cause T_{v+}^* to move upward. This will deteriorate the transient stability of the system.

As shown in Fig. 6c, when the minimum value of T_{v-}^- is greater than T_{v-}^* , the negative sequence unbalanced torque (ΔT_{v-}) will make $|\omega_{pll}^-|$ and ω_{pll}^+ increase continuously. Finally, the LOS phenomena for angular frequency increase will occur in the negative and positive sequence PLL. In this case, the positive sequence coupled components will cause T_{v-}^* to move downward, and deteriorate the transient stability of the system.

In Fig. 6d, if the maximum value of T_{v-}^- is less than T_{v-}^* , there will be no intersection between T_{v-}^- and T_{v-}^* . In this case, the surplus ΔT_{v-} will lead to the LOS phenomena for angular frequency decrease in the negative and positive sequence PLL. In addition, the positive sequence coupled components will prevent T_{v-}^* from moving upward. This is beneficial to the transient stability of the system.

In conclusion, the interaction between the positive and negative sequence components will significantly affect the balance of the virtual torque for the system, and have different effects on the existence of positive and negative

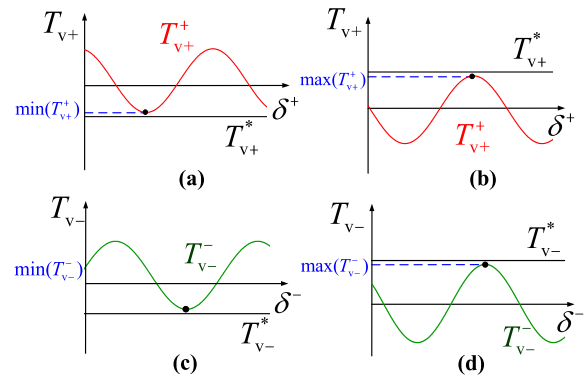


Fig. 6 a The $\min(T_{v+}^+)$ is greater than T_{v+}^* . b The $\max(T_{v+}^+)$ is less than T_{v+}^* . c The $\min(T_{v-}^-)$ is greater than T_{v-}^* . d The $\max(T_{v-}^-)$ is less than T_{v-}^*

sequence equilibrium points under different working conditions. T_{v+}^+ , T_{v+}^* , T_{v-}^- , and T_{v-}^* must satisfy the Criterion (14) to ensure that the VSC system has the ability to achieve $T_{v\pm}^* = T_{v\pm}^\pm$. Otherwise, the positive and negative sequence equilibrium points of the VSC system will not exist, and the LOS phenomena will inevitably occur.

$$\begin{aligned} & [\max(T_{v+}^+) \geq T_{v+}^*] \cap [\min(T_{v+}^+) \leq T_{v+}^*] \\ & \& [\max(T_{v-}^-) \geq T_{v-}^*] \cap [\min(T_{v-}^-) \leq T_{v-}^*] \end{aligned} \quad (14)$$

The existence of positive and negative sequence equilibrium points of the system can be effectively evaluated by criterion (14). However, even if criterion (14) is satisfied, inappropriate transient synchronous behavior of the VSC system may lead to the deceleration or acceleration area insufficient to reach the desired equilibrium point during the transient synchronous process [9]. Therefore, it is necessary to further discuss the influence of control switching action detection delay and the interaction between the positive and negative sequence PLL on the synchronous capability of the VSC system.

3.3 Equal area criterion of the VSC system during asymmetric grid faults

Integrating both sides of (10) with δ^+ and δ^- , respectively, yields (15), where δ_t^+ and δ_t^- are the respective equivalent power angles of the positive and negative sequence operating points at any instant.

In (15), ΔE_{p+} and ΔE_{p-} are the virtual potential energies due to the change of current references. ΔE_{K+}^+ and ΔE_{K-}^- are the virtual kinetic energy variations of the positive and negative sequence operating points, respectively, and represent the motion state of $\omega_{pll}^\pm$ relative to ω_g . ΔE_{K+}^- and ΔE_{K-}^+ are the virtual kinetic energy variations generated by the positive and negative sequence coupled components, respectively. ΔE_{D+}^+ and ΔE_{D-}^- are the energies

consumed by the positive and negative virtual damping, respectively, while ΔE_{D+} and ΔE_{D-} are the respective energies consumed by the positive and negative coupled virtual damping.

$$\begin{cases} \underbrace{\int_{\delta_0^+}^{\delta_t^+} (T_{v+}^* - T_{v+}^+) d\delta^+}_{\Delta E_{P+}} - \underbrace{\int_{\delta_0^+}^{\delta_t^+} D_{v+}^+ (\omega_{pll}^+ - \omega_g) d\delta^+}_{\Delta E_{D+}} + \underbrace{\int_{\delta_0^+}^{\delta_t^+} D_{v+}^- (\omega_{pll}^- + \omega_g) d\delta^+}_{\Delta E_{D-}} \Big|_{\delta_0^+}^{\delta_t^+} d\delta^+ = \underbrace{\frac{1}{2} \omega_b J_{v+}^+ (\omega_{pll}^+ - \omega_g)^2 \Big|_{\delta_0^+}^{\delta_t^+}}_{\Delta E_{K+}^+} + \underbrace{\frac{1}{\omega_b} \int_{\delta_0^+}^{\delta_t^+} J_{v+}^- \delta^- \Big|_{\delta_0^+}^{\delta_t^+} d\delta^+}_{\Delta E_{K-+}} \\ \underbrace{\int_{\delta_0^-}^{\delta_t^-} (T_{v-}^* - T_{v-}^-) d\delta^-}_{\Delta E_{P-}} - \underbrace{\int_{\delta_0^-}^{\delta_t^-} D_{v-}^- (\omega_{pll}^- + \omega_g) d\delta^-}_{\Delta E_{D-}} + \underbrace{\int_{\delta_0^-}^{\delta_t^-} D_{v-}^+ (\omega_{pll}^+ - \omega_g) d\delta^-}_{\Delta E_{D+}} \Big|_{\delta_0^-}^{\delta_t^-} d\delta^- = \underbrace{\frac{1}{2} \omega_b J_{v-}^- (\omega_{pll}^- + \omega_g)^2 \Big|_{\delta_0^-}^{\delta_t^-}}_{\Delta E_{K-}^-} + \underbrace{\frac{1}{\omega_b} \int_{\delta_0^-}^{\delta_t^-} J_{v-}^+ \delta^+ \Big|_{\delta_0^-}^{\delta_t^-} d\delta^-}_{\Delta E_{K-+}} \end{cases} \quad (15)$$

In addition, Eq. (15) indicates that because of the existence of coupled relationship between the positive and negative sequence components, the negative sequence components will affect not only the dynamic change of positive sequence equivalent power angle (δ^+), but also the conversion relationship among the virtual kinetic, potential and damping energies during the transient synchronous process of the positive sequence operating point, and vice versa. Therefore, the influence mechanism of the coupled relationship between the positive and negative sequence PLL on the synchronous capability of the VSC system needs to be further explored.

3.4 Influence on the synchronous process of the positive and negative sequence operating points

According to (10) and (13), the synchronous processes of the positive and negative sequence operating points during asymmetric grid faults are shown in Figs. 7 and 8, respectively.

In Fig. 7, $B_0 \rightarrow B_1$ is the fault detection stage, T_{v0+}^* and T_{v0} are the virtual prime mover torque and electromagnetic torque at the pre-fault stage, respectively. T_{v1+}^* is the positive sequence virtual prime mover torque at the fault detection stage, and T_{v1+}^- is the positive sequence virtual electromagnetic torque during the SLG grid fault. In Fig. 8, $B'_0 \rightarrow B'_1$ is the fault detection stage, and point A'_0 is the initial point of the negative sequence operating point when the SLG fault occurs, which does not exist at the pre-fault stage. T_{v1-}^- is the negative sequence virtual electromagnetic torque during the SLG grid fault. T_{v1-}^* and T_{v2-}^* are the negative sequence virtual prime mover torque during the fault detection stage and fault duration stages respectively.

According to [14], the positive sequence operating point will accelerate at the fault detection stage and then decelerate during the $B_1 \rightarrow C_1$ stage. The energy

accumulated and released by the positive sequence operating point during the pre-fault to fault duration stage can be represented by the acceleration area (S_{acc1+}) and deceleration area (S_{dec1+}), respectively.

These are shown in Fig. 7. Similarly, according to (13), the negative sequence operating point will accelerate at the fault detection stage, and decelerate at the stage of $B'_1 \rightarrow C'_1$. From the pre-fault to fault duration stage, the energy accumulated and released by the negative sequence operating point can be represented by the acceleration area (S_{acc1-}) and the deceleration area (S_{dec1-}), as shown in Fig. 8.

It can be seen from (15) and Fig. 5 that if the unbalanced energy generated by the system during the fault detection stage cannot be completely consumed before δ^+ reaches δ_{max}^+ , or δ^- reaches δ_{max}^- , the LOS phenomena

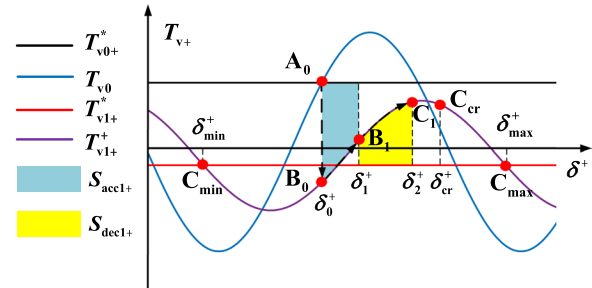


Fig. 7 Trajectories of the positive sequence operating point from pre-fault to fault duration stage

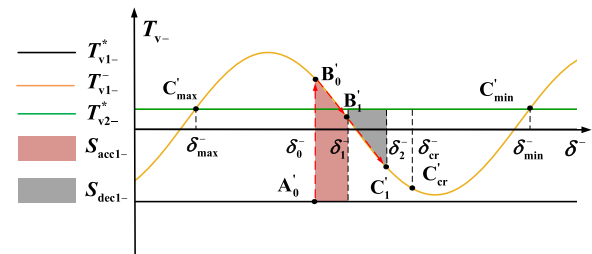


Fig. 8 Trajectories of the negative sequence operating point from pre-fault to fault duration stage

for angular frequency increase will occur in the positive and negative sequence PLL. Therefore, the transient stability criterion of the VSC system from the pre-fault to fault duration stage is expressed as:

$$t_{\text{cut}} = (\delta_{\text{cr}} - \delta_0^+) \sqrt{\frac{2J_{v1+}^+}{\omega_b(S_{\text{acc}1+} - \Delta E_{D1+})}} \quad (18)$$

$$\left\{ \begin{array}{l} S_{\text{acc}1+} = \int_{\delta_0^+}^{\delta_1^+} (T_{v0+}^* - T_{v1+}^+) d\delta^+ \\ \max(S_{\text{dec}1+}) = \int_{\delta_1^+}^{\delta_{\text{max}}^+} (T_{v1+}^+ - T_{v1+}^*) d\delta^+ \\ \max(\Delta E_{D1+}) = \int_{\delta_0^+}^{\delta_1^+} D_{v1+}^+ (\omega_{\text{pll}}^+ - \omega_g) d\delta^+ + \int_{\delta_1^+}^{\delta_{\text{max}}^+} D_{v2+}^+ (\omega_{\text{pll}}^+ - \omega_g) d\delta^+ \\ \quad + \int_{\delta_1^+}^{\delta_{\text{max}}^+} D_{v2+}^- (\omega_{\text{pll}}^- + \omega_g) \Big|_{\delta_1^-}^{\delta_{\text{min}}^-} d\delta^+ \\ S_{\text{acc}1-} = \int_{\delta_0^-}^{\delta_1^-} (T_{v1-}^- - T_{v1-}^*) d\delta^- \\ \max(S_{\text{dec}1-}) = \int_{\delta_1^-}^{\delta_{\text{min}}^-} (T_{v2-}^* - T_{v1-}^-) d\delta^- \\ \max(\Delta E_{D1-}) = \int_{\delta_0^-}^{\delta_1^-} D_{v1-}^+ (\omega_{\text{pll}}^+ - \omega_g) \Big|_{\delta_0^+}^{\delta_1^+} d\delta^- + \int_{\delta_0^-}^{\delta_1^-} D_{v1-}^- (\omega_{\text{pll}}^- + \omega_g) d\delta^- \\ \quad + \int_{\delta_1^-}^{\delta_{\text{min}}^-} D_{v2-}^- (\omega_{\text{pll}}^- + \omega_g) d\delta^- + \int_{\delta_1^-}^{\delta_{\text{min}}^-} D_{v2-}^+ (\omega_{\text{pll}}^+ - \omega_g) \Big|_{\delta_1^+}^{\delta_{\text{max}}^+} d\delta^- \\ S_{\text{acc}1+} \leq \max(S_{\text{dec}1+} + \Delta E_{D1+}) \&\& S_{\text{acc}1-} \leq \max(S_{\text{dec}1-} + \Delta E_{D1-}) \end{array} \right. \quad (16)$$

According to (10) and (16), from the pre-fault to fault duration stage, the existence of the positive and negative sequence coupled components will increase the positive and negative sequence deceleration areas and the energy consumed by the virtual damping. This will prevent the system from having angular frequency increase LOS phenomena.

As shown in (13), the positive sequence PLL plays a dominant role in the transient stability of the VSC system at the fault detection stage. Therefore, the critical fault clearance angle (δ_{cr}) of the system can be approximated as the critical fault clearance angle of the positive sequence operating point. Considering the influence of the negative sequence coupled components on the positive sequence critical fault clearance angle, the critical fault clearance angle (δ_{cr}) of the VSC system can be approximately derived from (17).

Equation (18) means that the fault detection time must be less than t_{cut} . Otherwise, there will be LOS phenomena for angular frequency increase in the positive and negative sequence PLL.

3.5 Transient stability enhancement control strategy based on improved PLL

From the above analysis, it can be deduced that during the asymmetric faults, irrational control strategy switching delay will lead to LOS phenomena of the system. Therefore, to improve the transient stability of the VSC system during asymmetric faults by increasing the critical fault clearance time and reducing the operation delay time of the system, a transient synchronous stability enhancement control strategy of the VSC system during asymmetric grid faults is studied in this section.

$$\delta_{\text{cr}} = \frac{U_{\text{gf}}^+ |K_1| (\cos(\delta_0^+ - \varphi_1) - \cos(\delta_{\text{max}}^+ - \varphi_1)) + (\omega_g L_{\text{eq}4} I_{\text{dm}} + \frac{k_p}{k_i} L_{\text{eq}4} I_{\text{dm}}) \delta_0^+}{\omega_g L_{\text{eq}4} I_{\text{dm}} - R_{\text{eq}4} I_{\text{q}+}^{++} - \omega_g L_{\text{eq}4} I_{\text{d}+}^{++} + R_{\text{eq}5} I_{\text{q}+}^{*-} + \omega_g L_{\text{eq}5} I_{\text{d}+}^{*-} + \frac{k_p}{k_i} (L_{\text{eq}4} (I_{\text{dm}} - I_{\text{d}+}^{++}) - L_{\text{eq}5} I_{\text{d}+}^{*-} (\omega_{\text{pll}}^- + \omega_g) \Big|_{\delta_1^-}^{\delta_2^-})} \\ - \frac{((R_{\text{eq}4} I_{\text{q}+}^{++} + \omega_g L_{\text{eq}4} I_{\text{d}+}^{++} + \frac{k_p}{k_i} (L_{\text{eq}4} I_{\text{d}+}^{++} + L_{\text{eq}5} I_{\text{d}+}^{*-} (\omega_{\text{pll}}^- + \omega_g) \Big|_{\delta_1^-}^{\delta_2^-}) - R_{\text{eq}5} I_{\text{q}+}^{*-} - \omega_g L_{\text{eq}5} I_{\text{d}+}^{*-}) \delta_{\text{max}}^+}{\omega_g L_{\text{eq}4} I_{\text{dm}} - R_{\text{eq}4} I_{\text{q}+}^{++} - \omega_g L_{\text{eq}4} I_{\text{d}+}^{++} + R_{\text{eq}5} I_{\text{q}+}^{*-} + \omega_g L_{\text{eq}5} I_{\text{d}+}^{*-} + \frac{k_p}{k_i} (L_{\text{eq}4} (I_{\text{dm}} - I_{\text{d}+}^{++}) - L_{\text{eq}5} I_{\text{d}+}^{*-} (\omega_{\text{pll}}^- + \omega_g) \Big|_{\delta_1^-}^{\delta_2^-})} \quad (17)$$

Simplifying (17) and then combined with (16), the critical fault clearance time (t_{cut}) can be obtained, and is expressed as:

It can be seen from (16)–(18) that the larger J_{v+}^+ or ΔE_{D+} is, the slower the change of δ^+ and the larger the allowable the critical fault clearance time angle or time

are. This makes the transient synchronous ability of the system stronger during asymmetric grid faults. To increase J_{v+}^+ and ΔE_{D+} of the VSC system, the deviations between ω_{pll}^+ and ω_g , and between ω_{pll}^- and ω_g are fed back negatively to the positive and negative sequence q-axis voltages by the amplifying parameters, respectively, as shown in Fig. 9.

According to (4) and Fig. 9, the positive and negative sequence q-axis voltage can be expressed as:

$$\begin{cases} u_{tq+}^+ = -U_{gf}^+ |K_1| \cos(\delta^+ - \varphi_1) + L_{eq4} I_{d+}^+ \omega_{pll}^+ - L_{eq5} I_{d+}^- \omega_{pll}^- - k_{pf}(\omega_{pll}^+ - \omega_g) \\ u_{tq-}^- = -U_{gf}^- |K_7| \cos(\delta^- - \varphi_7) + L_{eq9} I_{d-}^- \omega_{pll}^- - L_{eq10} I_{d-}^+ \omega_{pll}^+ - k_{pf}(\omega_{pll}^- + \omega_g) \end{cases} \quad (19)$$

By substituting (19) into (10), Eq. (20) can be obtained.

Under the control structure of Fig. 9, the critical fault clearance time of the system can be expressed as:

$$\begin{cases} \hat{t}_{cut}^+ = (\delta_{cr}^+ - \delta_0^+) \sqrt{\frac{2J_{v1+}^+ + J_{vf+}^+}{\omega_b(S_{acc1+} - \Delta E_{D1+} - \Delta E_{Df+})}} \\ \Delta E_{Df+} = \int_{\delta_0^+}^{\delta_1^+} D_{vf+}^+ (\omega_{pll}^+ - \omega_g) d\delta^+ + \int_{\delta_1^+}^{\delta_{max}^+} D_{vf+}^+ (\omega_{pll}^+ - \omega_g) d\delta^+ \end{cases} \quad (21)$$

As shown in (21), the introduction of the negative feedback branches can effectively increase t_{cut} of the system, which means that the system can maintain transient synchronous stability with the grid under a longer fault

detection delay. Therefore, this method can improve the transient synchronous stability of the VSC system during asymmetric grid faults.

$$\begin{cases} \left(\underbrace{\left(\frac{1 - k_p L_{eq4} I_{d+}^+}{k_i} + \frac{k_{pf}}{k_i} \right)}_{J_{v+}^+} \frac{d\omega_{pll}^+}{dt} + \underbrace{\frac{k_p L_{eq5} I_{d+}^-}{k_i}}_{J_{v+}^+} \frac{d\omega_{pll}^-}{dt} = \underbrace{R_{eq4} I_{q+}^+ + \omega_g L_{eq4} I_{d+}^+ - R_{eq5} I_{q+}^- - \omega_g L_{eq5} I_{d+}^-}_{T_{v+}^+} - \underbrace{U_{gf}^+ |K_1| \sin(\delta^+ - \varphi_1)}_{T_{v+}^+} + \underbrace{\left(\frac{k_p}{k_i} L_{eq5} I_{d+}^- \right)}_{D_{v+}^+} (\omega_{pll}^- - \omega_g) \right. \\ \left. - \underbrace{\left(\frac{k_p}{k_i} (\omega_b U_{gf}^+ |K_1| \cos(\varphi_1 - \delta^+) - L_{eq4} I_{d+}^+) + k_{pf} \right)}_{D_{v+}^+ + D_{vf+}^+} (\omega_{pll}^+ - \omega_g) \right) \\ \left(\underbrace{\left(\frac{1 - k_p L_{eq9} I_{d-}^-}{k_i} + \frac{k_{pf}}{k_i} \right)}_{J_{v-}^-} \frac{d\omega_{pll}^-}{dt} + \underbrace{\frac{k_p L_{eq10} I_{d-}^+}{k_i}}_{J_{v-}^-} \frac{d\omega_{pll}^+}{dt} = \underbrace{(R_{eq9} I_{q-}^- - \omega_g L_{eq9} I_{d-}^- - R_{eq10} I_{q-}^+ - \omega_g L_{eq10} I_{d-}^+)}_{T_{v-}^-} - \underbrace{U_{gf}^- |K_7| \sin(\delta^- - \varphi_7)}_{T_{v-}^-} + \underbrace{\left(\frac{k_p}{k_i} L_{eq10} I_{d-}^+ \right)}_{D_{v-}^-} (\omega_{pll}^+ - \omega_g) \right. \\ \left. - \underbrace{\left(\frac{k_p}{k_i} (\omega_b U_{gf}^- |K_7| \cos(\varphi_7 - \delta^-) - L_{eq9} I_{d-}^-) + k_{pf} \right)}_{D_{v-}^- + D_{vf-}^-} (\omega_g + \omega_{pll}^-) \right) \end{cases} \quad (20)$$

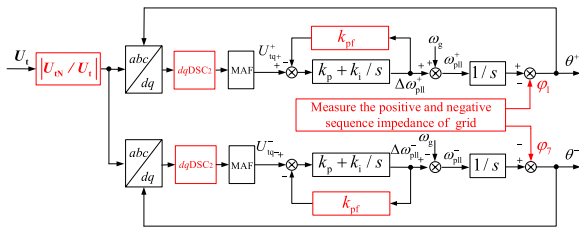


Fig. 9 The improved positive and negative sequence PLL control structure

In addition, reference [33] points out that the use of MAF in Fig. 4 will introduce signal delay to the system, which will reduce the dynamic response speed of the PLL. Therefore, an improved DSC-PLL structure is proposed based on delay signal cancellation (DSC) [34]. However, reference [34] does not consider the influence of voltage amplitude and phase angle mutation on the dynamic performance of PLL during grid fault, nor the differences of output characteristics between the positive and negative sequence PLL.

Therefore, this paper further considers the influence of K_i and ϕ_i in (3), by introducing the ratio between the rated voltage amplitude (U_{TN}) and the fault voltage amplitude (U_f), so the influence of the terminal voltage amplitude mutation on the transient characteristics of the positive and negative sequence PLL is weakened. Besides, to reduce the influence of the grid impedance phase angle mutation on the dynamic performance of the VSC system, the sequence impedance measurement method [35, 36] is used to introduce the positive and negative sequence impedance angles into the output angles of the positive and negative sequence PLL. Through the above methods, the dynamic response speed of the positive and negative sequence PLL for the VSC system can be further improved during asymmetric grid faults. The specific improvement measures are also shown in Fig. 9.

4 Simulation and experimental validation

4.1 Simulation validation

In order to verify the correctness of the theoretical analysis and the effectiveness of the enhancement control strategy, a simulation model of a 2 MW PLL-based VSC grid-connected system is established in MATLAB/Simulink, which is similar to Fig. 1. The main parameters of the simulated system are listed in Table 1 in the Appendix. At the pre-fault stage, $P_{ref}^* = 1.0$ p.u., and at $t = 1.0$ s, an SLG fault occurs at the 220 kV AC grid side, which causes $|U_{fa}|$ to drops to 0.32 p.u. The fault is cleared at $t = 2.0$ s.

When the fault detection delay of the VSC system is 5 ms, Figs. 10–12 show the three LOS phenomena under different LVRT current references, respectively.

Case 1: When the LVRT current references I_{d+}^{+*} , I_{q+}^{+*} , I_{d-}^{+*} , and I_{q-}^{+*} of the system are set to 0 p.u., 1 p.u., 0 p.u., and 0 p.u., respectively, according to criterion (14), the positive sequence equilibrium point of the system does not exist. Consequently, as shown in Fig. 10, the LOS phenomenon for angular frequency decrease occurs in the positive sequence PLL, and the LOS phenomenon

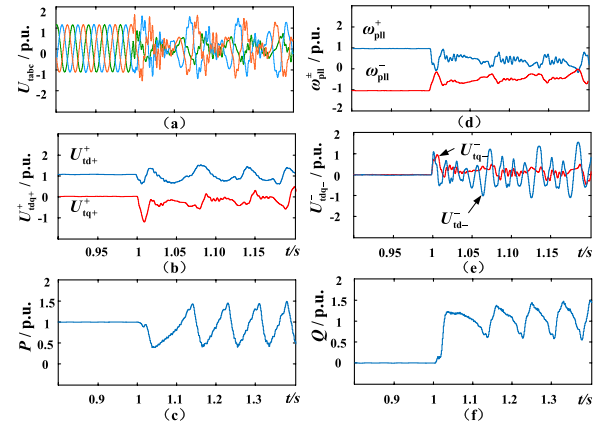


Fig. 10 Simulation results of Case 1. **a** Terminal three-phase voltage. **b** Positive sequence dq-axis voltage. **c** Output active power. **d** Positive and negative sequence PLL output frequency. **e** Negative sequence dq-axis voltage. **f** Output reactive power

also occurs in the negative sequence PLL owing to the influence of the positive sequence coupled components. Meanwhile, the output active and reactive power of the VSC system are also unstable due to the LOS of positive and negative sequence PLL.

Case 2: The LVRT current references I_{d+}^{+*} , I_{q+}^{+*} , I_{d-}^{+*} , and I_{q-}^{+*} are set to -1.0 p.u., 0.15 p.u., 0 p.u., and 0.15 p.u., respectively, and according to Criterion (16), the calculation result of S_{acc1+} is 0.233 , while ΔS_{1+} ($\Delta S_{1+} = S_{dec+} + \Delta E_{D1+}$) is 0.186 . As shown in Fig. 11, due to the excessive acceleration area S_{acc1+} , the LOS phenomena for angular frequency increase occur in the positive and negative sequence PLL. Consequently, the output active and reactive power of the VSC system are

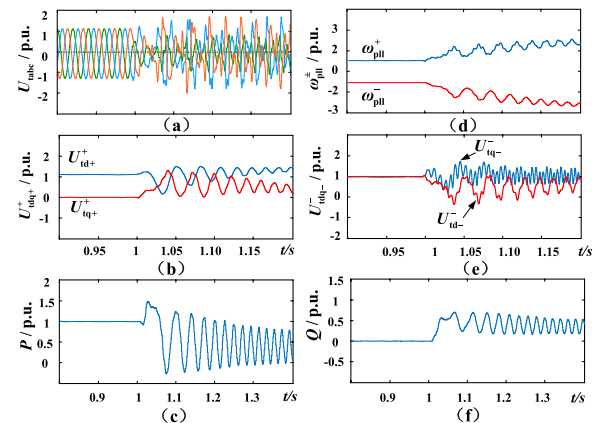


Fig. 11 Simulation results of Case 2. **a** Terminal three-phase voltage. **b** Positive sequence dq-axis voltage. **c** Output active power. **d** Positive and negative sequence PLL output frequency. **e** Negative sequence dq-axis voltage. **f** Output reactive power

Table 1 Parameters of the simulation VSC system

Parameter	Value	Parameter	Values
Rated Power	2 MW	$Z_{T1}^{+} = Z_{T1}^{-} = Z_{T1}^0$	$0.005 + j0.21$ p.u.
Rated voltage	220 kV	$Z_{T2}^{+} = Z_{T2}^{-} = Z_{T2}^0$	$0.018 + j0.29$ p.u.
DC-link voltage	1200 V	Fault impedance Z_f	0.00027 p.u.
C_f, L_f	0.0015 p.u., 0.002 p.u.	k_{p_PLL}, k_{i_PLL}	$0.8, 15$
$Z_L^{+} = Z_L^{-} = Z_L^0$	$0.0049 + j0.32$ p.u.	k_p, k_i of current loop	$2.5, 20$

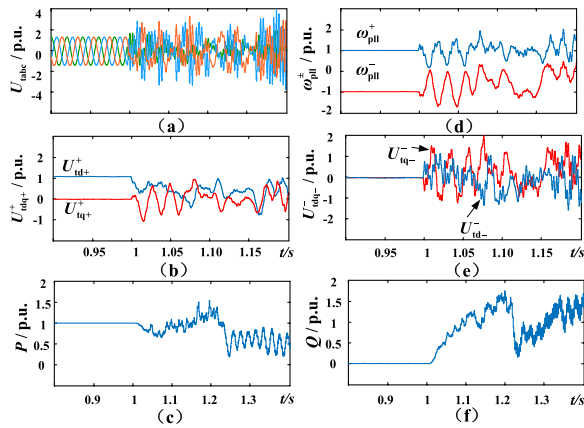


Fig. 12 Simulation results of Case 3. **a** Terminal three-phase voltage. **b** Positive sequence dq-axis voltage. **c** Output active power. **d** Positive and negative sequence PLL output frequency. **e** Negative sequence dq-axis voltage. **f** Output reactive power

also unstable due to the LOS of positive and negative sequence PLL.

Case 3: During LVRT, the I_{d+}^{+*} , I_{q+}^{+*} , I_{d-}^{-*} , and I_{q-}^{-*} are set to 0 p.u., 0.15 p.u., 0 p.u. and 0.95 p.u., respectively, according to criterion (14), the negative sequence equilibrium point of the system does not exist. Consequently, as shown in Fig. 12, the LOS phenomena for angular frequency decrease occur in the positive and negative sequence PLL. This also leads to the instability of the

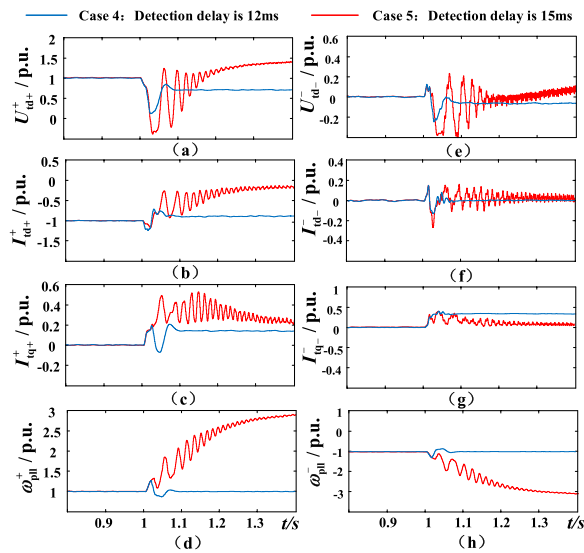


Fig. 13 Simulation results under different detection delays. **a** Positive sequence d-axis voltage. **b** Positive sequence d-axis current. **c** Positive sequence q-axis current. **d** Positive sequence PLL output frequency. **e** Negative sequence d-axis voltage. **f** Negative sequence d-axis current. **g** Negative sequence q-axis current. **h** Negative sequence PLL output frequency

output active and reactive power of the VSC system. These simulation results are consistent with the analysis results.

Figure 13 shows the simulation results of the system under the same working conditions but different fault detection delays. In Fig. 13, the LVRT current references I_{d+}^{+*} , I_{q+}^{+*} , I_{d-}^{-*} , and I_{q-}^{-*} are set to -0.75 p.u., 0.35 p.u., 0 p.u., and 0.3 p.u., respectively. In this case, according to criterion (18), t_{cut} is 13.48 ms, which is larger than the fault detection delay of 12 ms in Case 4. It means that the energy accumulated by the positive and negative sequence operating points at the fault detection stage can be consumed completely during the fault duration stage. Therefore, the system can maintain synchronous stability with the grid. However, when the fault detection time increases to 15 ms in Case 5, criterion (18) is not satisfied. As shown in Case 5, the LOS phenomena for angular frequency increase occur in the positive and negative sequence PLL. Therefore, the criterion 18 can effectively evaluate the ability of the VSC system to maintain transient synchronous stability with the grid.

To further demonstrate the effectiveness of the improved positive and negative sequence PLL control strategy, simulations with the different control strategies are performed and the results are shown in Fig. 14. As shown, the LVRT references are set to the same as those in Fig. 13, but the fault detection time is 13 ms. Under these conditions, when the traditional DDSRF-PLL control strategy is adopted [25, 26], the LOS phenomena occur in the system, which can be seen in Fig. 14. However, when the conventional dual-sequence PLL control strategy without the proposed improvement is adopted, the LOS phenomenon does not occur in the system despite a long transient regulation process. This is because the negative sequence component also has the

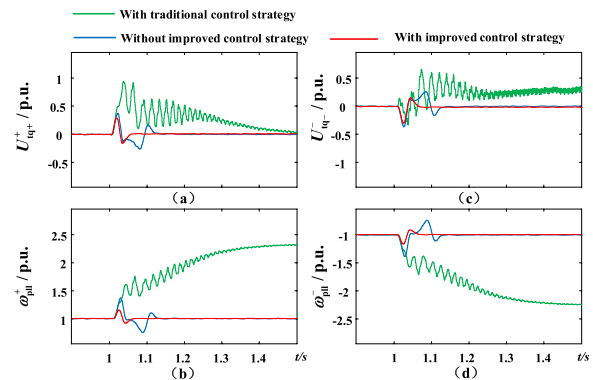


Fig. 14 Simulation results under different control strategies. **a** Positive sequence q-axis voltage. **b** Positive sequence PLL output frequency. **c** Negative sequence q-axis voltage. **d** Negative sequence PLL output frequency

ability to maintain transient synchronization with the grid, and this extends t_{cut} of the system.

In addition, as shown in Fig. 14, compared with the original control strategy. Since the improved positive and negative sequence PLL control strategy increases the virtual inertia and damping coefficient of the system, as well as compensating for the mutation of terminal voltage amplitude and phase angle during the fault, and reduces the influence of the response delay caused by MAF on the transient response characteristics of the system, it can effectively reduce the transient regulation time. It also enhances the transient synchronization performance, and increase the critical fault removal time of the system during the SLG fault. Therefore, the enhanced control strategy proposed in this paper is more beneficial for the VSC system to maintain transient synchronous stability with the grid. The simulation results are consistent with the analytical results.

Figures 15 and 16 show the simulation results of the VSC system with and without the enhancement control strategy under a two-phase-to-ground fault and two-phase fault respectively.

In Fig. 15, the two-phase-to-ground fault occurs at the 220 kV AC grid side, and the grid voltage $|U_{\text{ab}}|$ drops to 0.6 p.u. In this case, the fault detection delay of the system is set to 9 ms, and the LVRT current references $I_{\text{d+}}^{+*}$, $I_{\text{q+}}^{+*}$, $I_{\text{d-}}^{-*}$, and $I_{\text{q-}}^{-*}$ are set to -0.4 p.u., 0.5 p.u., 0 p.u., 0.5 p.u., respectively. Under these conditions, it can be seen from (18) that the t_{cut} of the system is 8.44 ms without the improved control strategy. Consequently, the LOS phenomena occurs in the system. However, when the enhancement control strategy is adopted, according to (21), t_{cut} is extended to 9.62 ms, and the system does not have the LOS phenomenon.

Similarly, in Fig. 16, the two-phase fault occurs at the 220 kV AC grid side, and the grid voltage $|U_{\text{ab}}|$ drops to

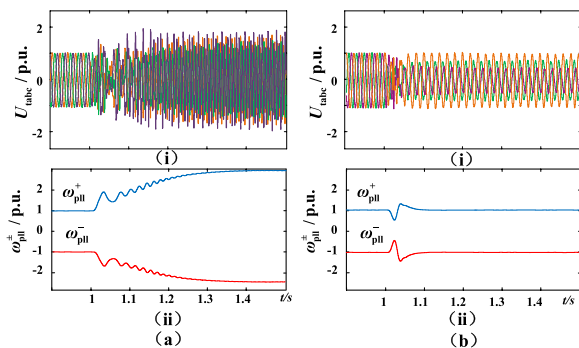


Fig. 15 Simulation results under two-phase-to-ground fault. **a** Without improved control strategy. **b** With improved control strategy. **i** Terminal three-phase voltage. **ii** Positive and negative sequence PLL output frequency

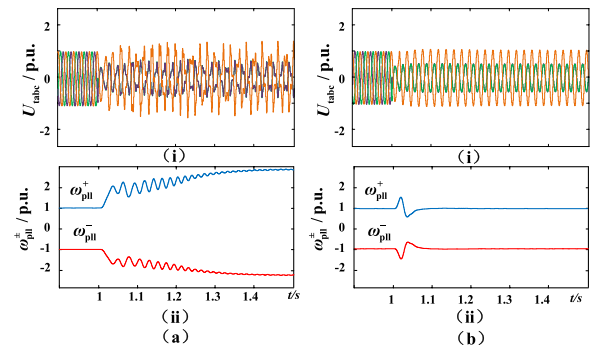


Fig. 16 Simulation results under two-phase fault. **a** Without enhancement control strategy. **b** With enhancement control strategy. **i** Terminal three-phase voltage. **ii** Positive and negative sequence PLL output frequency

0.63 p.u. In this case, during LVRT, if the fault detection delay of the system is set to 11 ms and the current references $I_{\text{d+}}^{+*}$, $I_{\text{q+}}^{+*}$, $I_{\text{d-}}^{-*}$, and $I_{\text{q-}}^{-*}$ are set to -0.5 p.u., 0.4 p.u., 0 p.u., 0.45 p.u., respectively, t_{cut} is 10.62 ms without the enhancement control strategy, and thus the LOS phenomena occur. However, t_{cut} is extended to 11.35 ms with the enhancement control strategy, and the system can maintain transient synchronization with the grid. Therefore, the improved positive and negative sequence PLL control strategy can effectively increase t_{cut} and enhance the transient stability margin of the VSC system under different types of asymmetric grid faults.

4.2 Experimental validation

To verify the accuracy of the theoretical analysis and the effectiveness of the enhancement control strategy, a 2.0 kW grid-connected VSC system is configured. The detailed experimental setup is shown in Fig. 17. As illustrated in Sect. 2, the generating source can be reasonably assumed as an ideal DC voltage source, so the DC-link voltage can be maintained stable during the grid faults. Therefore, an ideal DC voltage source is used in the experiments [37]. Besides, the reactors are used to simulate the transmission line and an oscilloscope is used to

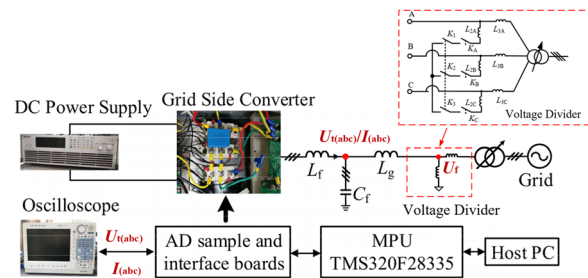
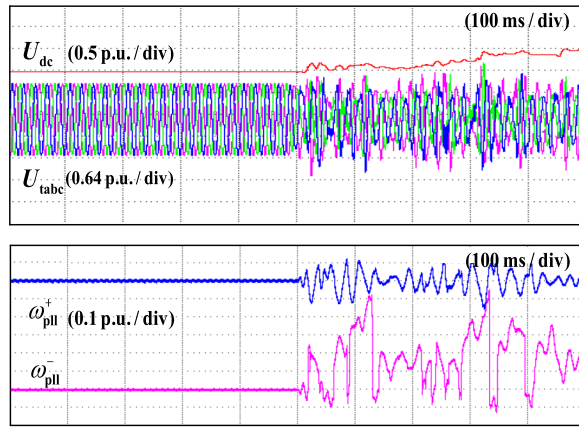


Fig. 17 Configuration of the grid-connected VSC system

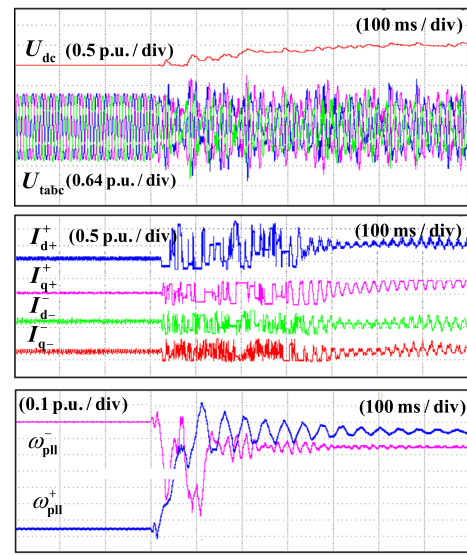
Table 2 Parameters of the experimental VSC system

Parameter	Value	Parameter	Values
Rated Power	2 kW (1 p.u.)	$Z_L^+ = Z_L^- = Z_L^0$	0.14 + j0.52 p.u.
Rated voltage	220 V (1 p.u.)	$Z_T^+ = Z_T^- = Z_T^0$	0.0036 + j0.026 p.u.
DC-link voltage	400 V	k_p, k_i of current loop	0.8, 0.1
Rated frequency	50 Hz (1 p.u.)	k_{p_PLL}, k_{i_PLL}	0.018, 0.002
$L_f^+ = L_f^-$	5 mH	$k_{p_PLL_fault}^+, k_{i_PLL_fault}^+$	0.02, 0.005
$C_f^+ = C_f^-$	30 μ F	$k_{p_PLL_fault}^-, k_{i_PLL_fault}^-$	0.025, 0.005

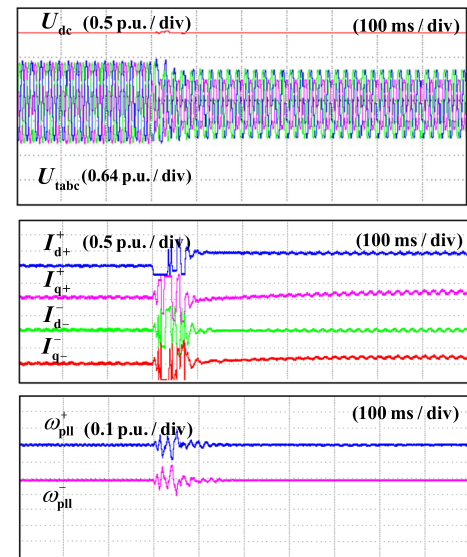
**Fig. 18** Experimental results of Case 1, the LOS phenomenon of the system caused by the absence of negative sequence equilibrium point

measure the data, and the asymmetrical grid fault is realized by the voltage divider. The main parameters of the experimental system are listed in Table 2 in the Appendix. Owing to the limitation of the laboratory conditions, only the experiments for the two-phase fault are presented here. Using the voltage divider, a two-phase short-circuit fault occurs in the AC grid at $t = 1.1$ s, causing the grid voltage $|\mathbf{U}_{ab}|$ to drop to 0.2 p.u., and the fault is cleared at $t = 2.1$ s.

Figure 18 presents the experimental results of the LOS phenomena of the system caused by the absence of negative sequence equilibrium point during the two-phase fault. In Case 1, the LVRT current references $I_{d+}^{+*}, I_{q+}^{+*}, I_{d-}^{+*}$, and I_{q-}^{+*} are set to -0.15 p.u., 0.2 p.u., 0 p.u. and 0.9 p.u., respectively. According to (10), the maximum value of T_{v-}^- is lower than T_{v-}^* , which causes criterion (14) no to be satisfied and the LOS phenomenon for angular frequency decrease in the negative sequence PLL. Due to the coupled characteristics between the positive and negative components, the LOS phenomenon also occurs in the positive sequence PLL. The experimental results are in agreement with the analytical and simulation results.



(a)



(b)

Fig. 19 Experimental results under different control strategies. **a** Case 3: without the improved control strategy. **b** Case 4: with the improved control strategy

Figure 19 presents two cases of experimental results for the VSC system without and with the enhancement control strategy. The fault detection time of the system is set to 12 ms, and the LVRT current instructions I_{d+}^{+*} , I_{q+}^{+*} , I_{d-}^{-*} , and I_{q-}^{-*} are set to -0.65 p.u., 0 p.u., 0.3 p.u., respectively. In these conditions, the critical fault clearance time of the system calculated by criterion (18) is 11.24 ms. It can be seen from Case 3 that the LOS phenomena occur. However, when the enhancement control strategy is adopted in Case 4, according to (21), t_{cut} is extended to 12.52 ms, which is greater than the fault detection time. Therefore, the LOS phenomenon does not occur.

All the experimental results are in agreement with the analytical and simulation results. In addition, since the differences among different types of asymmetric grid faults are mainly reflected in the structure of the sequence domain circuit, this does not affect the modeling of the two-phase fault or two-phase-to-ground fault. Therefore, the model and conclusions drawn in this paper are applicable to different types of asymmetric grid faults.

5 Conclusion

In this paper, the transient synchronous stability of the PLL-based grid-connected VSC system during asymmetric grid faults is studied. Through theoretical analysis, simulation and experimental verification, the following conclusions are drawn:

1. The interaction mechanism between the positive and negative sequence PLL during asymmetric grid faults is analyzed. The results show that under the action of the impedance network, the coupled relationship will be generated between the positive and negative

sequence components. Consequently, the dynamic behaviors of the positive and negative sequence PLL will affect each other. Therefore, the VSC system can maintain transient stability with the grid only when there are no LOS phenomena both in the positive and negative sequence PLL.

2. The influence of sequence control switching action, detection delay and interaction between the positive and negative sequence PLL on the transient synchronous stability of the VSC system are analyzed, and the criteria of transient synchronous stability based on the critical fault removal angle and time are derived. The analysis results show that the imbalance of the virtual energy accumulated or released by the positive and negative sequence operating points, and long detection delay can lead to LOS phenomena. Besides, the instability criteria proposed in this paper can effectively evaluate the transient synchronous stability of the VSC system during asymmetric grid faults.
3. A transient synchronous stability enhancement control strategy based on improved positive and negative sequence PLL for the VSC system during asymmetric grid faults is proposed. Simulation and experimental results show that the proposed method can effectively increase the critical fault removal time of the system, and enhance the transient synchronous stability of the VSC system under different types of asymmetric grid faults.

Appendix

Detailed expressions of coefficient in (3).

$$\begin{aligned}
 K_1 &= \frac{Z_t^+}{Z_1^+ + Z_t^+}, K_2 = \frac{Z_t^+}{Z_1^+ + Z_t^+ + 3Z_f + Z_1^0}, K_3 = \frac{Z_t^+}{Z_1^+ + Z_t^+ + 3Z_f + Z_t^- + Z_1^-}, \\
 K_6 &= \frac{Z_t^-}{Z_1^- + Z_t^- + 3Z_f + Z_1^0}, K_7 = \frac{Z_t^-}{Z_1^- + Z_t^-}, K_8 = \frac{Z_t^-}{Z_1^+ + Z_t^+ + 3Z_f + Z_t^- + Z_1^-}, \\
 Z_4 &= Z_t^+ / (Z_1^+ + Z_t^0 + Z_t^- + 3Z_f + Z_1^-), \\
 Z_5 &= (Z_t^- / (Z_1^+ + Z_t^0 + Z_t^+ + 3Z_f + Z_1^-)) \frac{Z_t^+}{Z_1^+ + Z_t^+ + Z_t^0 + 3Z_f + Z_t^-}, \\
 Z_9 &= Z_t^- / (Z_1^- + Z_t^0 + Z_t^+ + 3Z_f + Z_1^+), \\
 Z_{10} &= (Z_t^+ / (Z_1^+ + Z_t^0 + Z_t^- + 3Z_f + Z_1^-)) \frac{Z_t^-}{Z_1^+ + Z_t^+ + Z_t^0 + 3Z_f + Z_1^-}.
 \end{aligned}$$

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Author contributions

All authors contributed to the research. YL modeled the system, designed the algorithm of control strategy, and wrote the paper. JY, SH, and SC put forward the initial concept and gave technical guidance in the whole process. ZC, and QZ check the data and experimental results, and demonstrate the strategy proposed in the article. ZQ contributed to the revision and typesetting of the manuscript. All authors read and approved the final manuscript.

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Availability of data and materials

All data that support the finding of this study are included in this manuscript and its supplement information files.

Declarations

Competing interests

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this article.

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